

Compal Confidential

NTV00

LA-5662 Rev 0.2 Schematics

Intel PineView Processor/ Tiger point

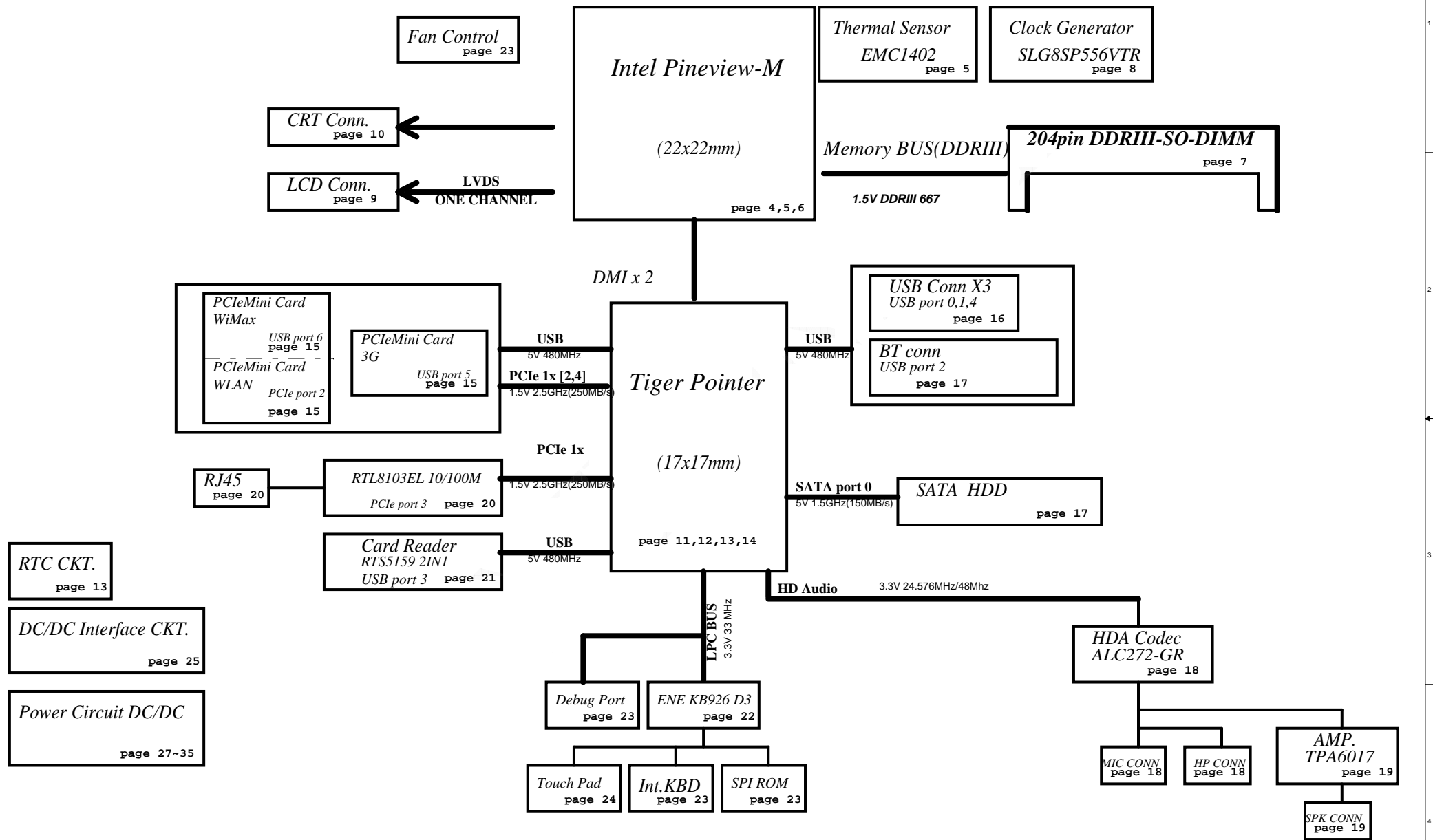
2010-05-10 REV : 1.0

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Model Name : NTV00

File Name : LA-5662P



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Voltage Rails

Power Plane	Description	S1	S3	S5	G3
VIN	Adapter power supply (19V)	ON	ON	ON	OFF
B+	AC or battery power rail for power circuit.	ON	ON	ON	ON
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF	OFF
+0.89VS	0.89VS GFX support voltage	ON	OFF	OFF	OFF
+0.75VS	0.75V switched power rail for DDR terminator	ON	OFF	OFF	OFF
+1.05VS	VCCP switched power rail	ON	OFF	OFF	OFF
+1.5VS	1.5V switched power rail	ON	OFF	OFF	OFF
+1.5V	1.5V power rail for DDR	ON	ON	OFF	OFF
+1.8VS	1.8VS switched power rail	ON	OFF	OFF	OFF
+3VALW	3.3V always on power rail	ON	ON	ON	OFF
+3V_SB	3.3V power rail for LAN	ON	ON	OFF	OFF
+3V_LAN	3.3V power rail for LAN	ON	ON	OFF	OFF
+RTCVCC	RTC power	ON	ON	ON	ON
+3VS	3.3V switched power rail	ON	OFF	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON	OFF
+5V_SB	5V power rail for SB	ON	ON	OFF	OFF
+5VS	5V switched power rail	ON	OFF	OFF	OFF
+VSB	VSB always on power rail	ON	ON	ON	OFF

Note : ON* means that this power plane is ON only with AC power available, otherwise it is OFF.

EC SM Bus1 address

Device	Address	Device	Address
Smart Battery	0001 011X b	EMC1402	1001 010X b

EC SM Bus2 address

Tiger point SM Bus address

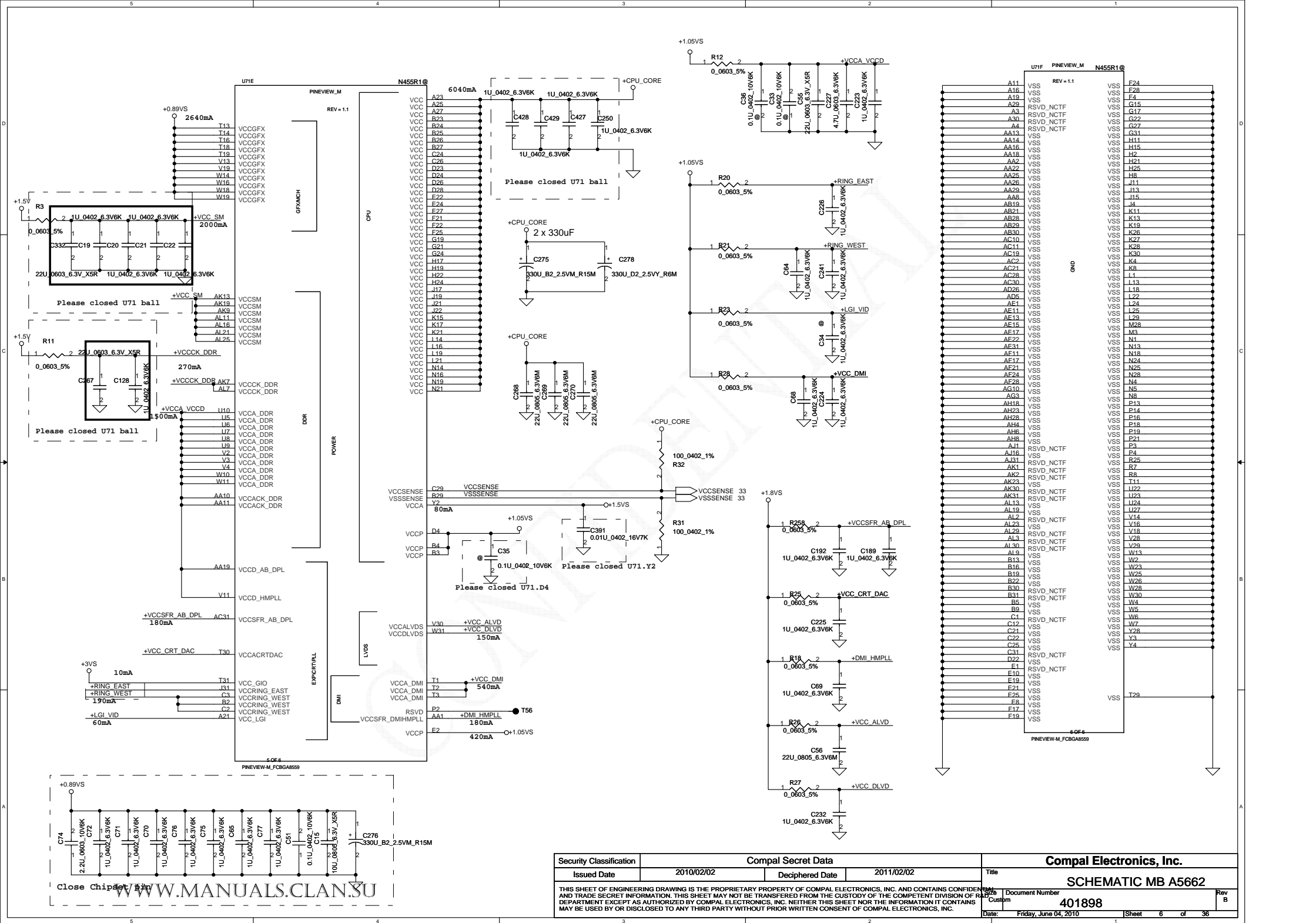
Device	Address
Clock Generator (SLG8SP556VTR)	1101 001Xb
DDR DIMMA	1010 000Xb

STATE \ SIGNAL	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS	Clock
Full ON	HIGH	HIGH	HIGH	ON	ON	ON	ON
S1(Power On Suspend)	HIGH	HIGH	HIGH	ON	ON	ON	LOW
S3 (Suspend to RAM)	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)	LOW	LOW	LOW	ON	OFF	OFF	OFF

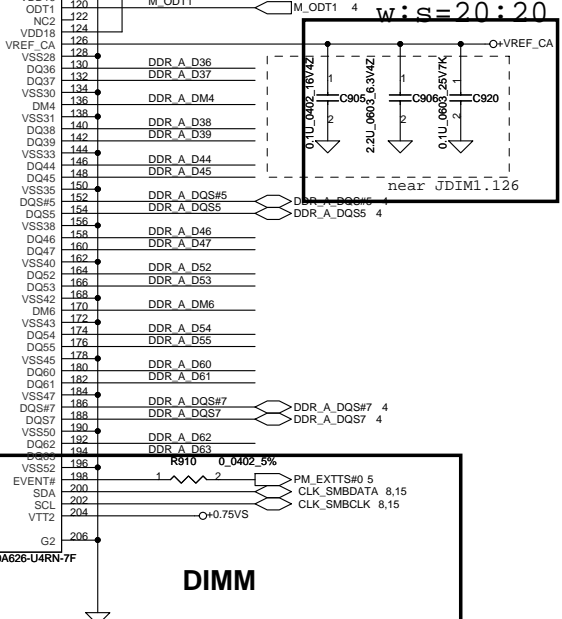
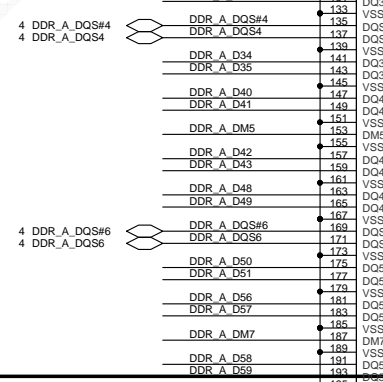
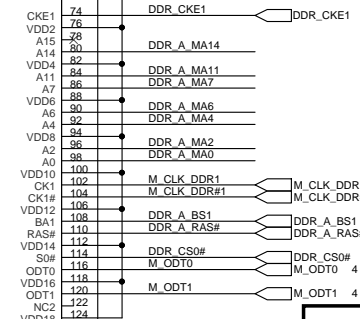
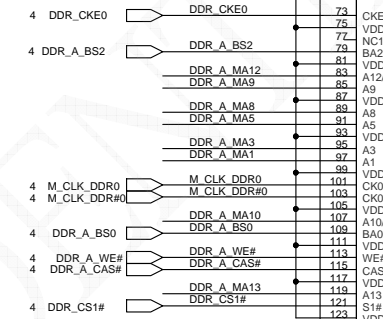
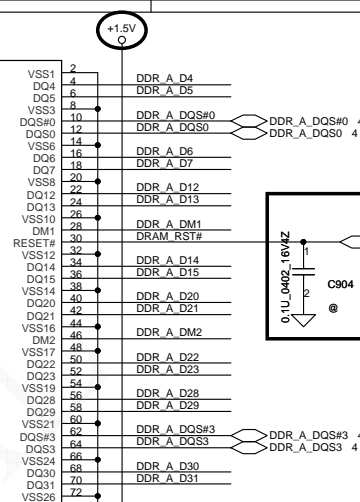
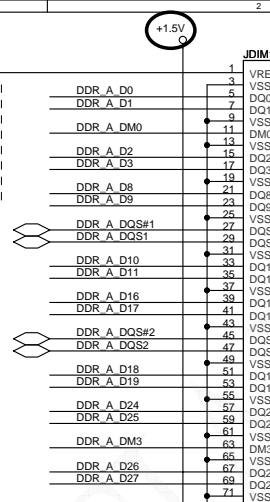
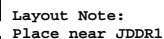
BTO Option Table

Function	Mini PCI-E SLOT				STAR			
description								
explain	Wi-Fi	WiMax	3G		POWER SAVING			
BTO	WLAN@	WIMAX@	3G@		STAR@			

Function			
description			
explain			
BTO			

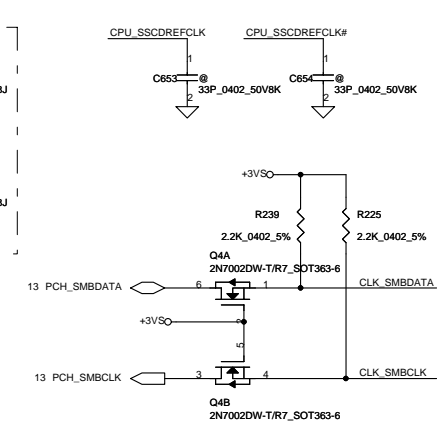
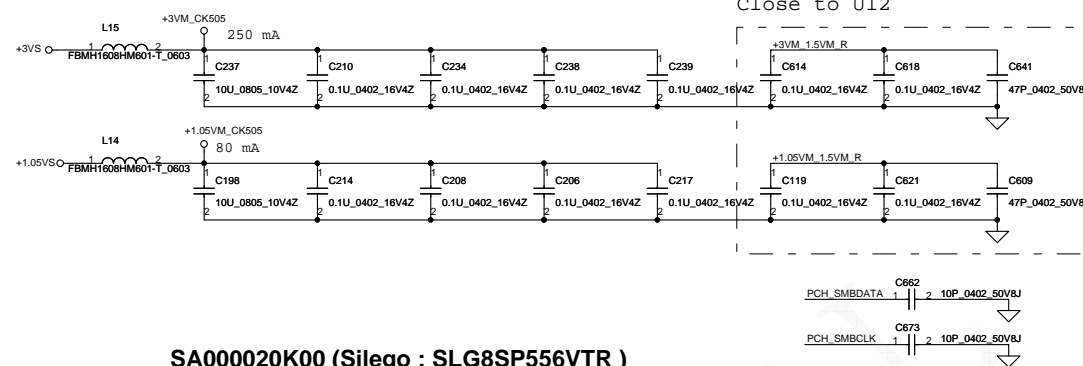


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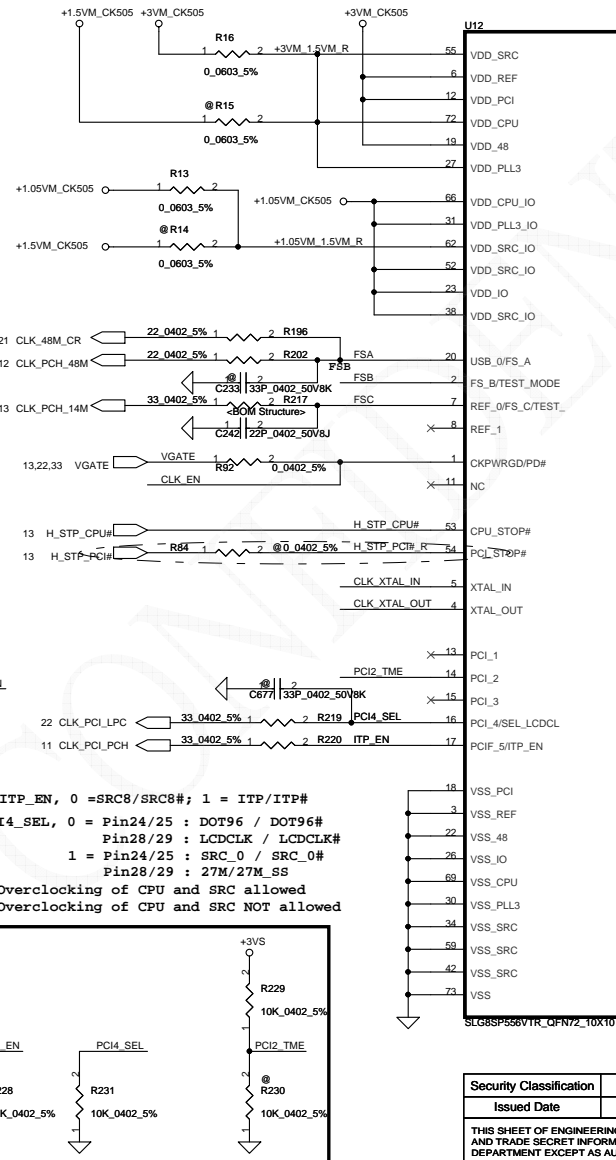
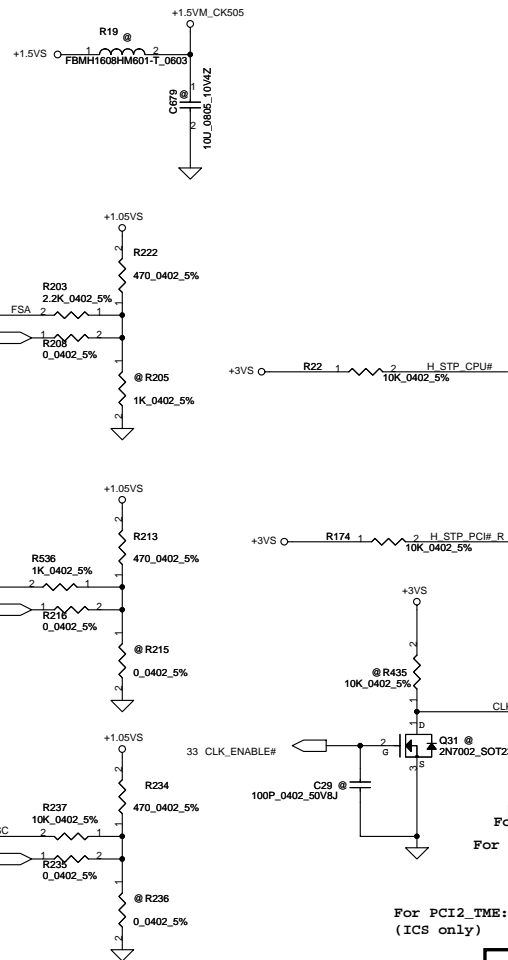


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FSC CLKSEL2	FSB CLKSEL1	FSA CLKSEL0	CPU MHz	SRC MHz	PCI MHz	REF MHz	DOT_96 MHz	USB MHz
0	0	0	266	100	33.3	14.318	96.0	48.0
0	0	1	133	100	33.3	14.318	96.0	48.0
0	1	0	200	100	33.3	14.318	96.0	48.0
0	1	1	166	100	33.3	14.318	96.0	48.0
1	0	0	333	100	33.3	14.318	96.0	48.0
1	0	1	100	100	33.3	14.318	96.0	48.0
1	1	0	400	100	33.3	14.318	96.0	48.0
1	1	1	Reserved					



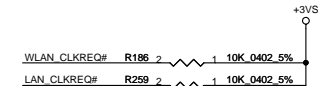
SA000020K00 (Silego : SLG8SP556VTR)



Signal	Pin	Function	Frequency
SDA	9	CLK_SMBDATA	7.15
SCL	10	CLK_SMBCLK	7.15
CPU_0	71	CLK_CPU_BCLK	5
CPU_0#	70	CLK_CPU_BCLK#	5
CPU_1	68	CLK_CPU_HPLCLK	5
CPU_1#	67	CLK_CPU_HPLCLK#	5
SRC_0/DOT_96	24	CPU_DREFCLK	5
SRC_0#DOT_96#	25	CPU_DREFCLK#	5
LCDCCLK/27M	28	CPU_SSCDREFCLK	5
LCDCCLK#/27M_SS	29	CPU_SSCDREFCLK#	5
SRC_2	32	CLK_CPU_EXP	4
SRC_2#	33	CLK_CPU_EXP#	4
SRC_3	35	X	
SRC_3#	36	X	
SRC_4	39	CLK_PCIE_SATA	11
SRC_4#	40	CLK_PCIE_SATA#	11
SRC_6	57	CLK_PCIE_WLAN	15
SRC_6#	56	CLK_PCIE_WLAN#	15
SRC_7	61	X	
SRC_7#	60	X	
SRC_8/CPU_ITP	64	X	
SRC_8#CPU_ITP#	63	X	
SRC_9	44	CLK_PCIE_LAN	20
SRC_9#	45	CLK_PCIE_LAN#	20
SRC_10	50	CLK_PCIE_PCH	12
SRC_10#	51	CLK_PCIE_PCH#	12
SRC_11	48	X	
SRC_11#	47	X	
CLKREQ_3#	37	X	
CLKREQ_4#	41	X	
CLKREQ_6#	58	WLAN_CLKREQ#	15
CLKREQ_7#	65	X	
CLKREQ_9#	43	LAN_CLKREQ#	20
SLKREQ_10#	49	X	
CLKREQ_11#	46	X	
USB_1/CLKREQ_A#	21	X	

SRC PORT LIST

PORT	DEVICE
SRC0	CPU_DREFCLK
SRC2	CPU_EXP
SRC3	
SRC4	PCIE_SATA
SRC6	PCIE_WLAN
SRC7	
SRC8	
SRC9	PCIE_LAN
SRC10	PCIE_PCH
SRC11	



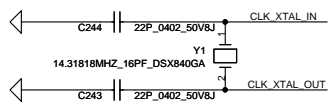
REQ PORT LIST

PORT	DEVICE
REQ_3#	
REQ_4#	
REQ_6#	PEIC_WLAN
REQ_7#	
REQ_9#	PCIE_LAN
REQ_10#	
REQ_11#	
REQ_A#	

```

For ITP_EN, 0 = SRC8/SRC8#, 1 = ITP/ITP#
For PCI4_SEL, 0 = Pin24/25 : DOT96 / DOT96#
                Pin28/29 : LCDCLK / LCDCLK#
                1 = Pin24/25 = SRC_0 / SRC_0#
                Pin28/29 : 27M/27M_SS
For PCI2_TME:0=Overclocking of CPU and SRC allowed
(ICS only) 1=Overclocking of CPU and SRC NOT allowed

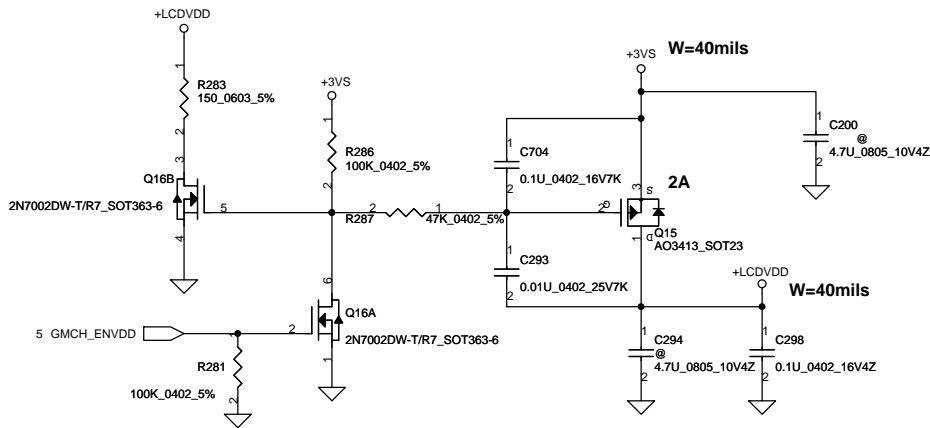
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Routing the trace at least 10mil

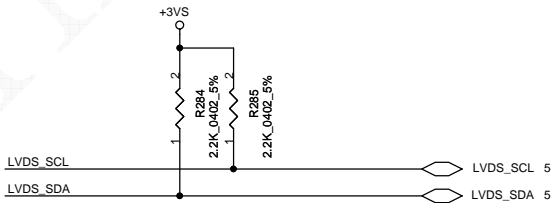
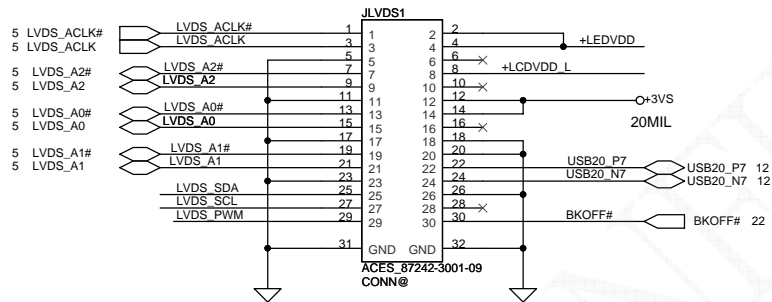
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LCD POWER CIRCUIT

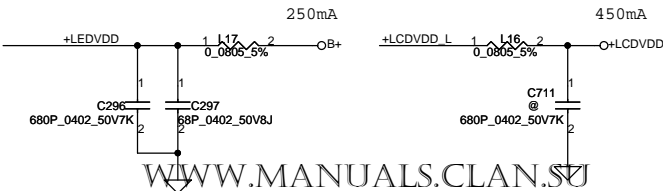
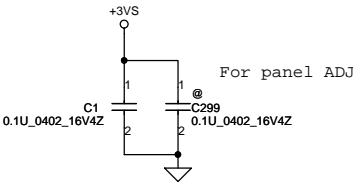
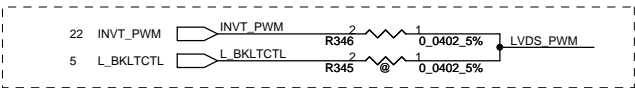


LED/PANEL BD. Conn.

Combine INT.CAMERA with LVDS

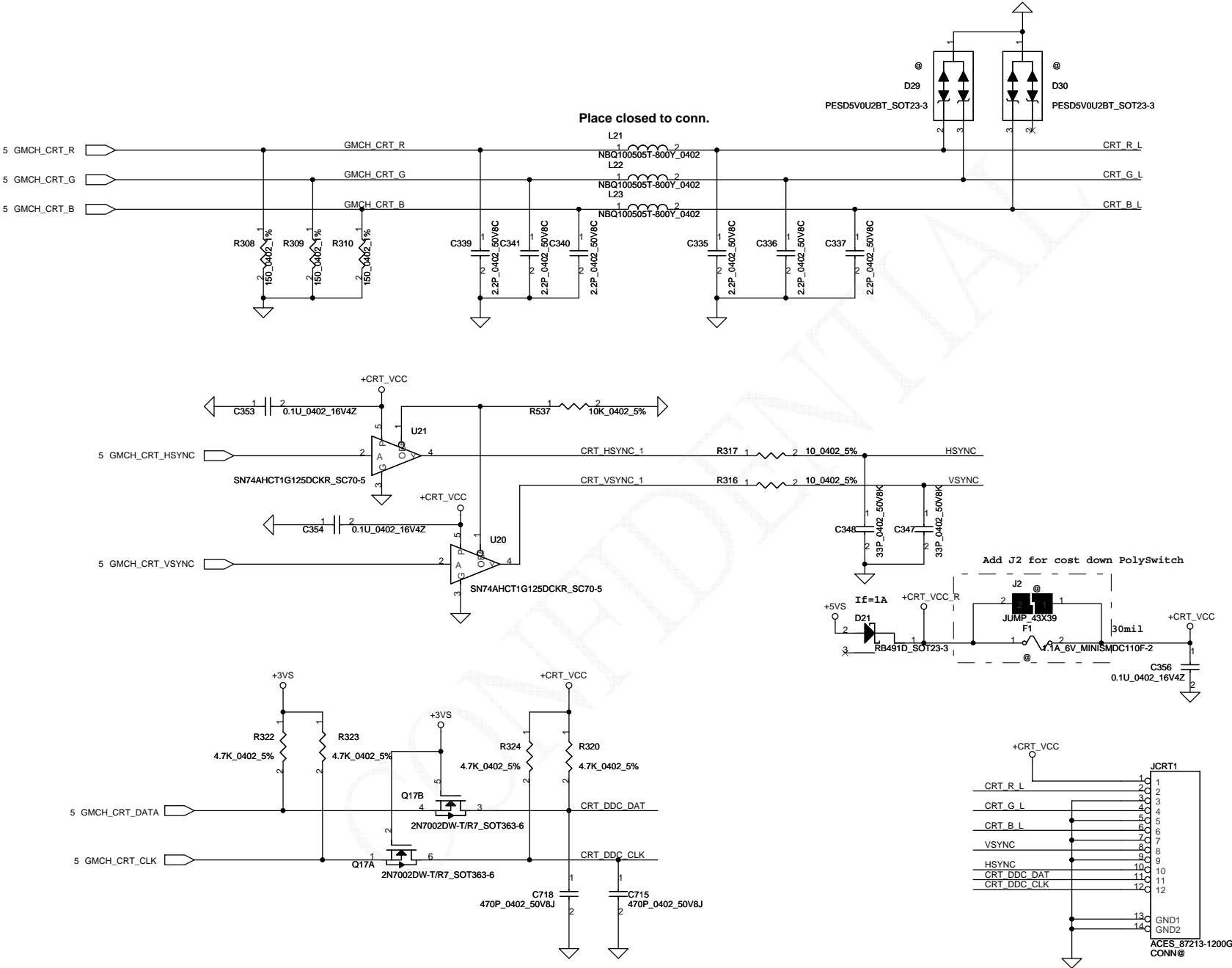


Option panel brightness control



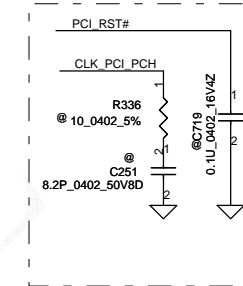
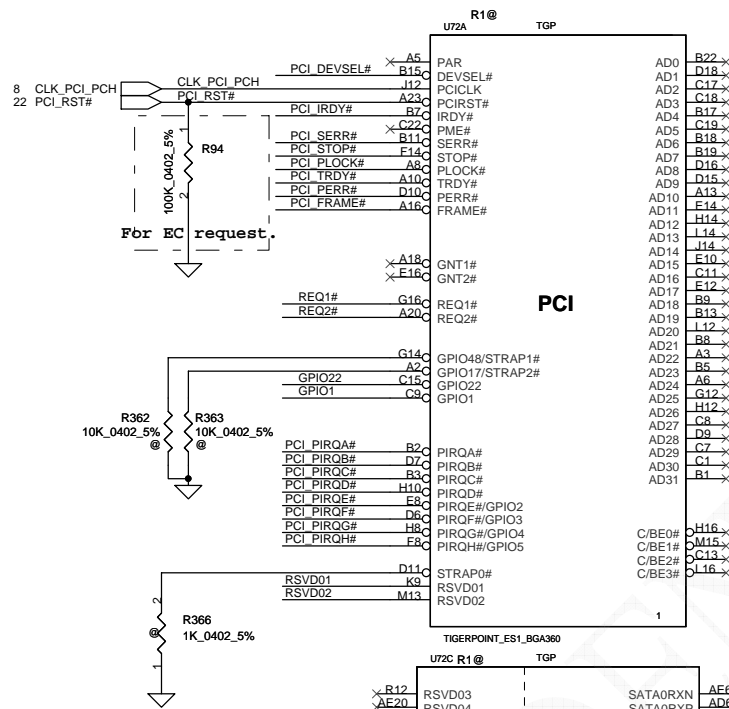
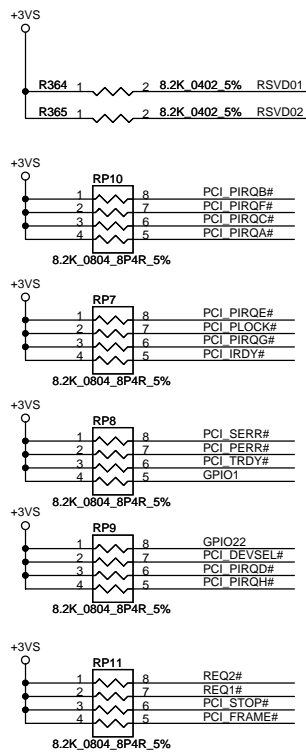
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CRT CONNECTOR

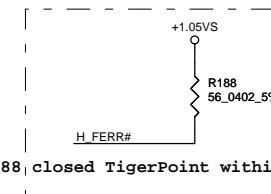


WWW.MANUALS.CLAN.SU

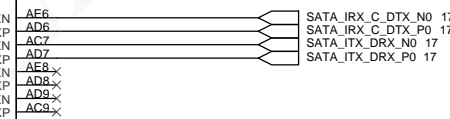
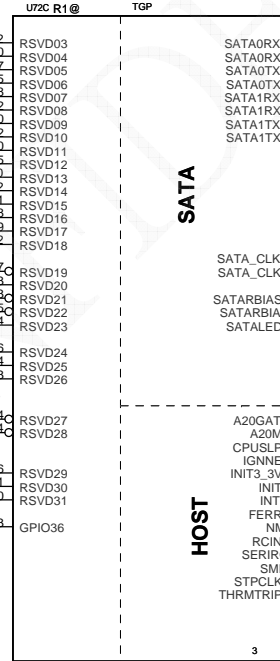
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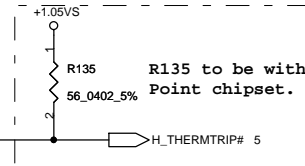
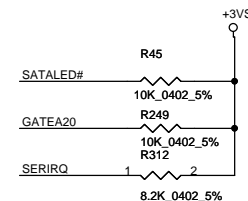
For EMI, close to TigerPoint



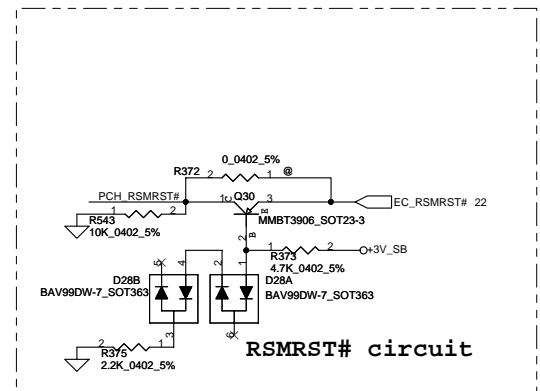
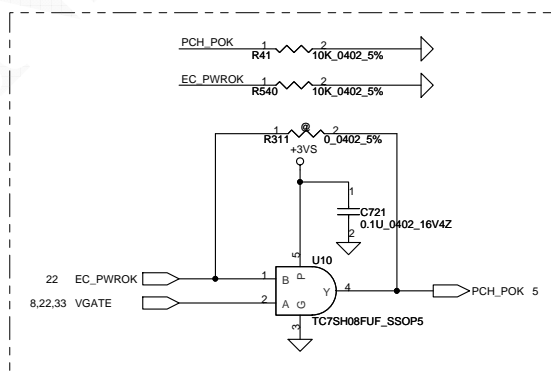
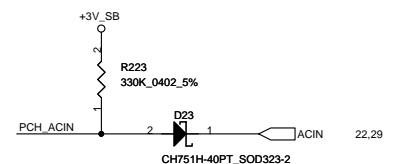
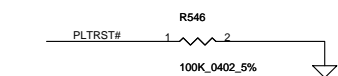
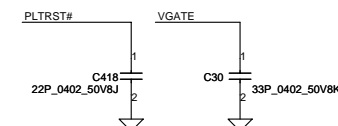
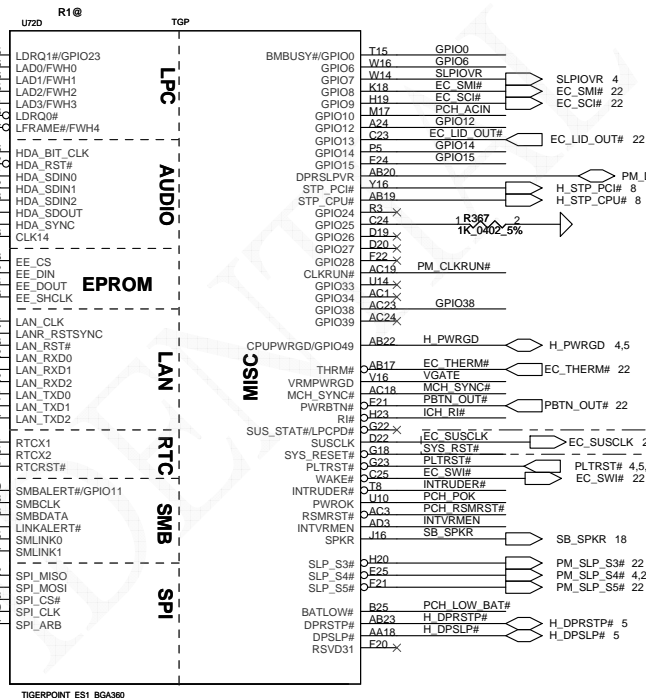
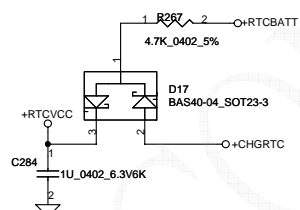
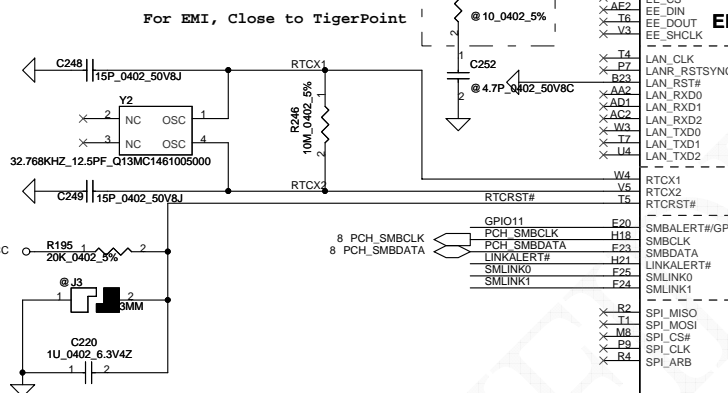
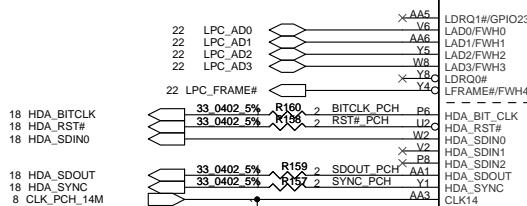
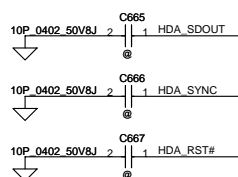
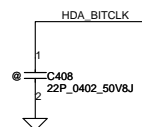
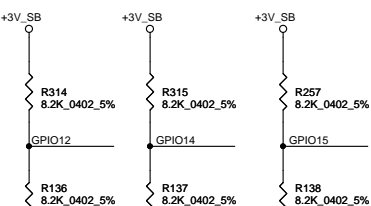
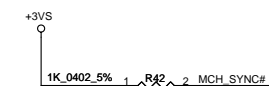
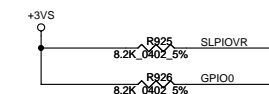
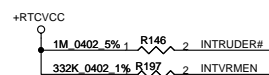
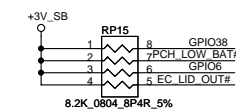
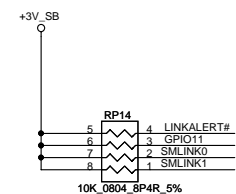
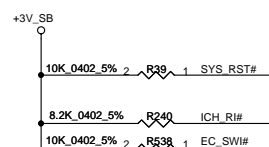
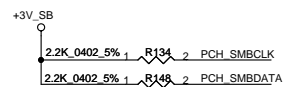
R188, closed TigerPoint within 1"



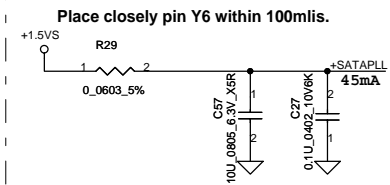
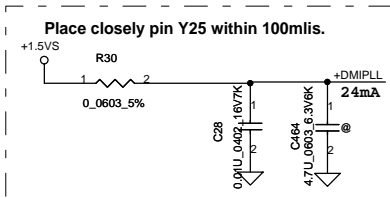
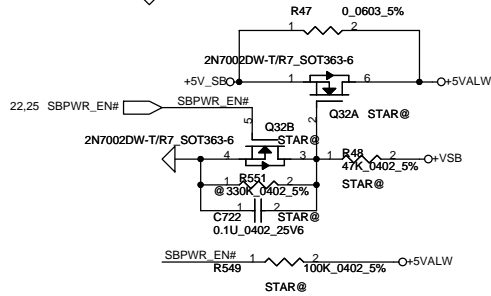
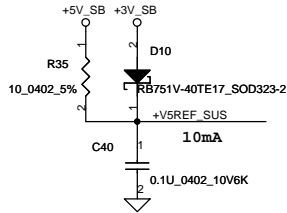
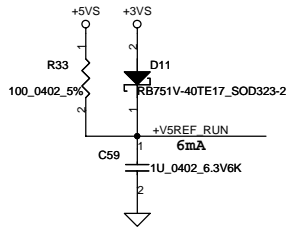
Please closed Tiger point PIN within 500 mils



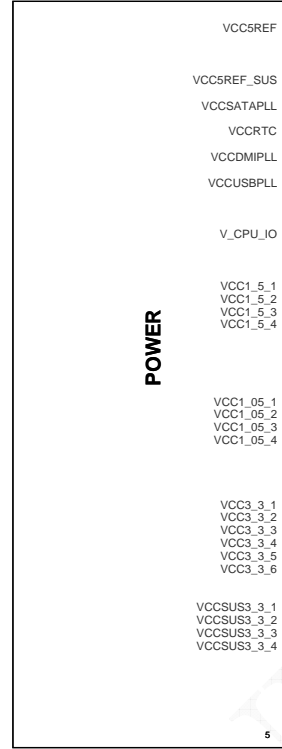
R135 to be within 1" from the Tiger Point chipset.



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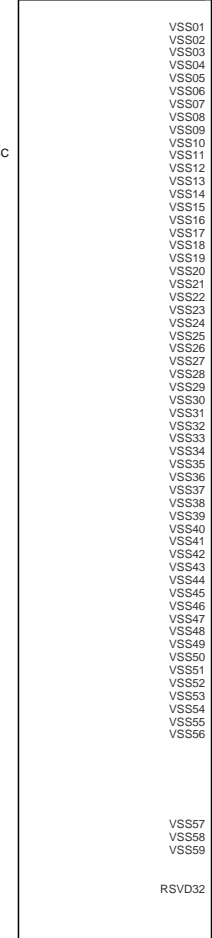


R1@ TGP



TIGERPOINT_ES1_BGA360

R1@ TGP



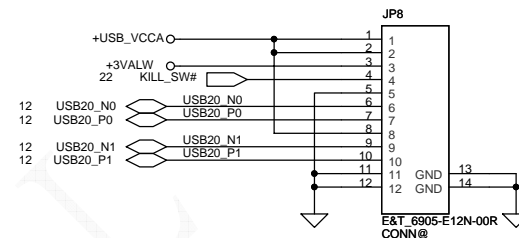
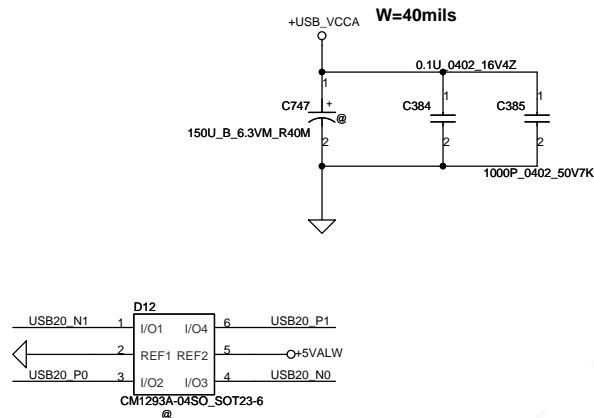
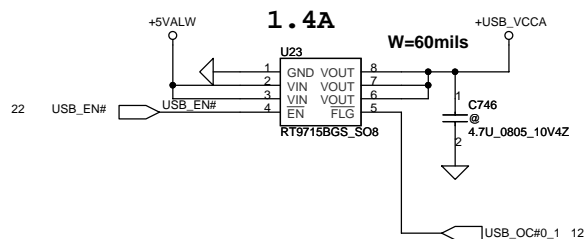
TIGERPOINT_ES1_BGA360

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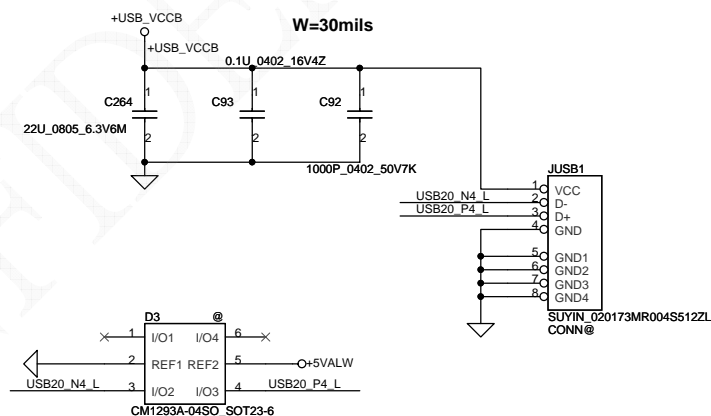
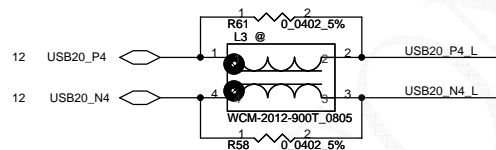
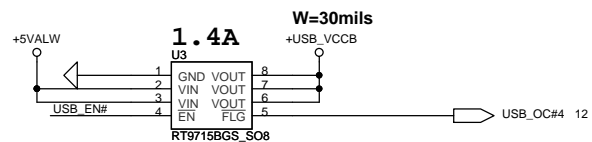
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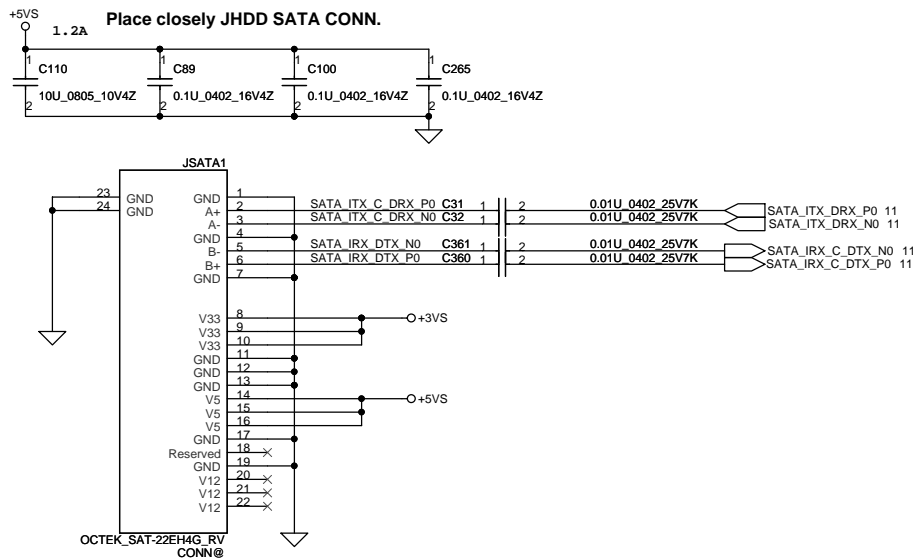
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USB X2

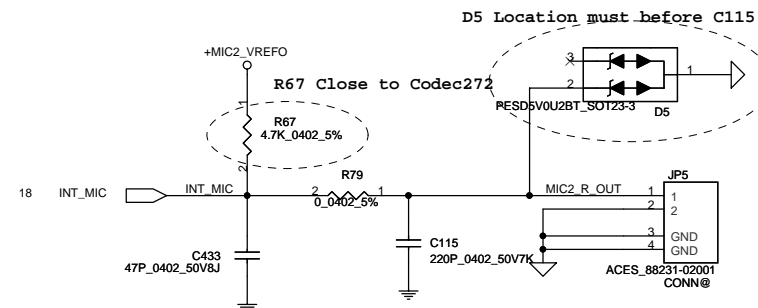
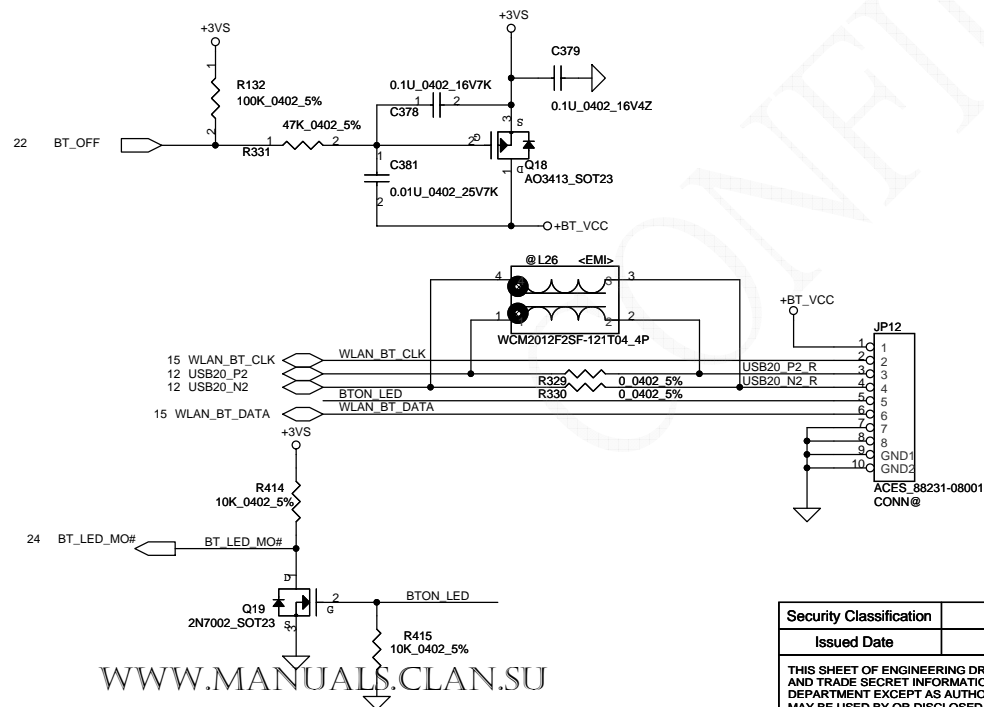


USB Board--Left



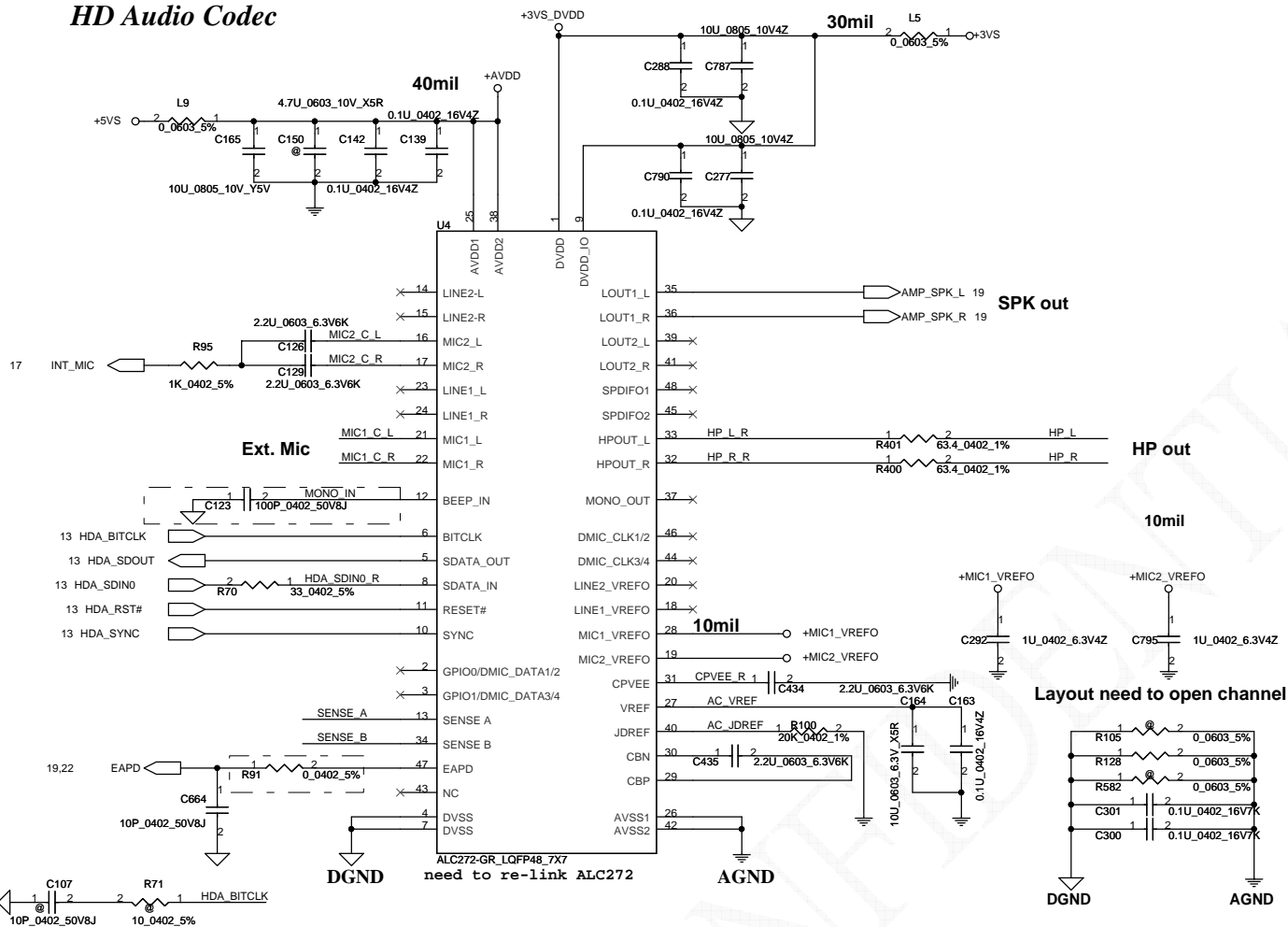
SATA Conn.

BlueTooth Interface

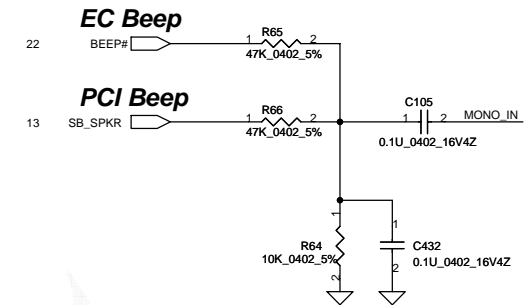


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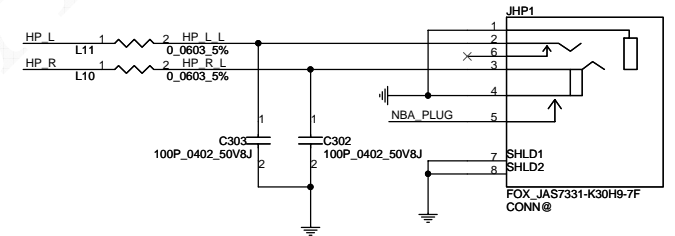
HD Audio Codec



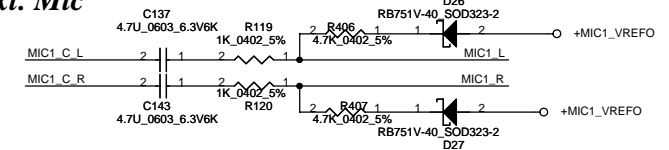
Beep sound



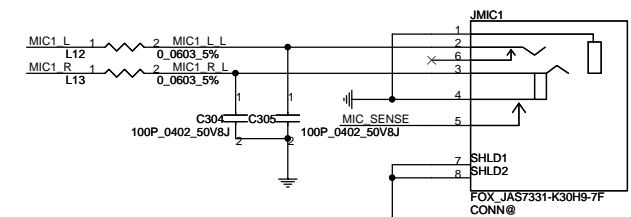
Head Phone JACK



Ext. Mic



Ex.MIC JACK

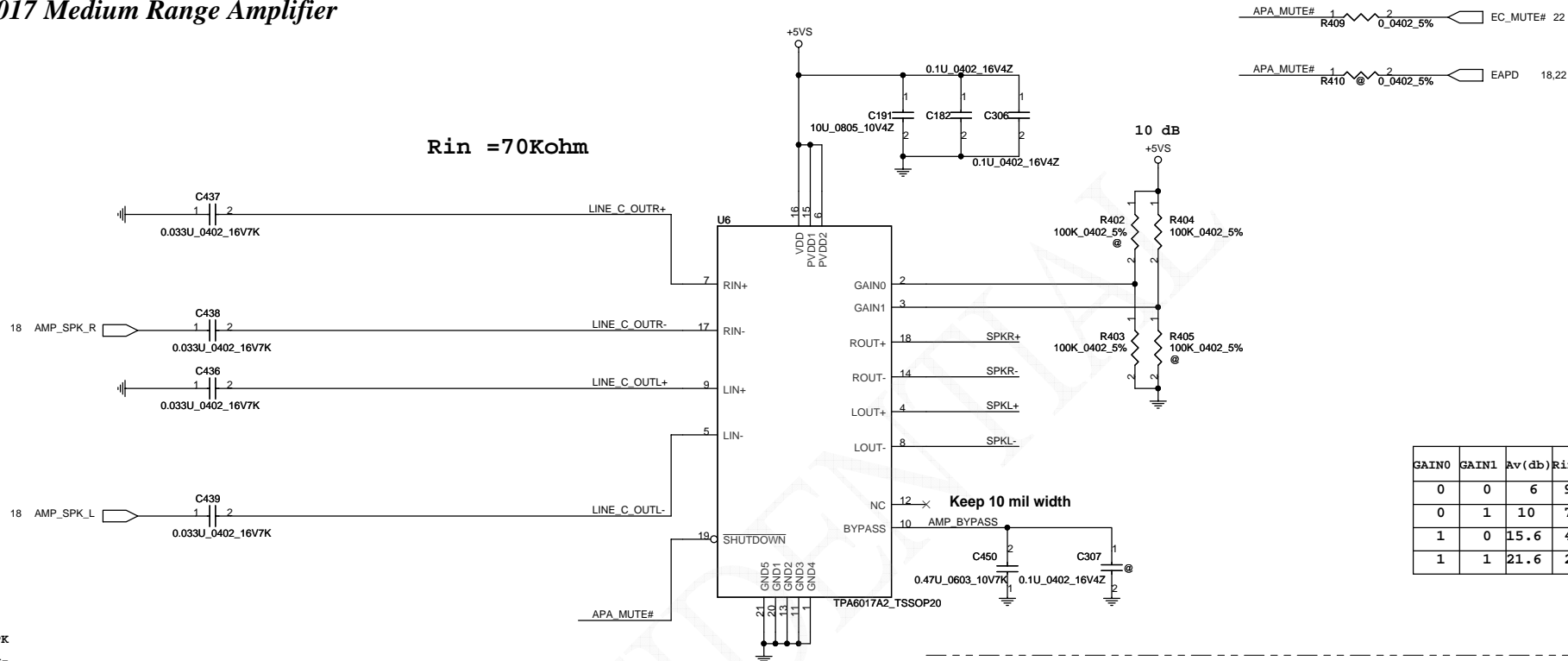


Sense Pin	Impedance	Codec Signals	Function
SENSE A	39.2K	PORT-A (PIN 39, 41)	Ext. MIC
	20K	PORT-B (PIN 21, 22)	
	10K	PORT-C (PIN 23, 24)	SPK out
	5.1K	PORT-D (PIN 35, 36)	
SENSE B	39.2K	PORT-E (PIN 14, 15)	Int. MIC
	20K	PORT-F (PIN 16, 17)	
	10K	PORT-H (PIN 37)	Headphone out
	5.1K	PORT-I (PIN 32, 33)	

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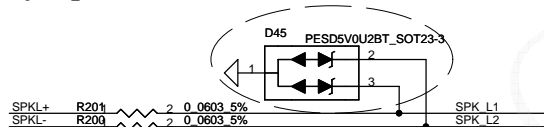
TPA6017 Medium Range Amplifier

Rin =70Kohm



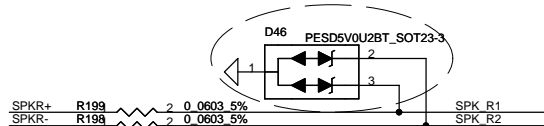
Use mono SPK
setting 68Hz
F=1/2nRC --> -3db
C=0.033U,R=70K,F=68Hz

Left Speaker Connector

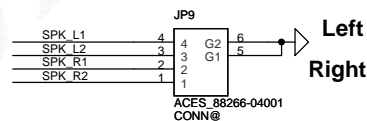


7/3 Change to SCA00000T00 for ESD request

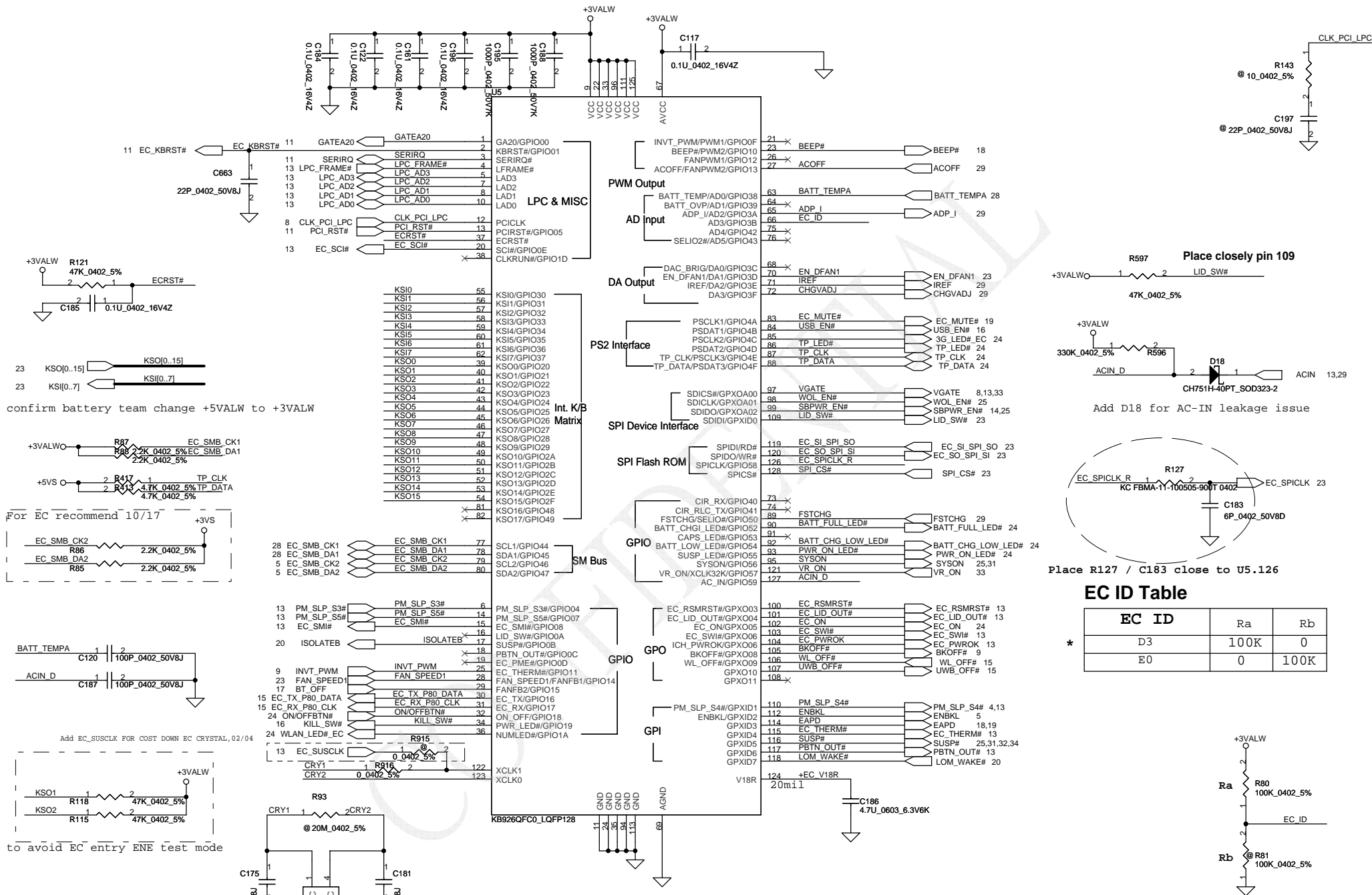
Right Speaker Connector



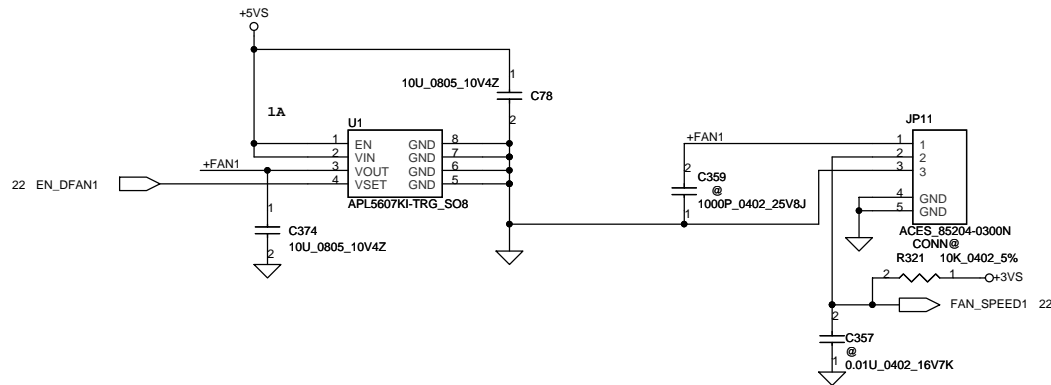
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7/3 Change to SCA00000T00 for ESD request



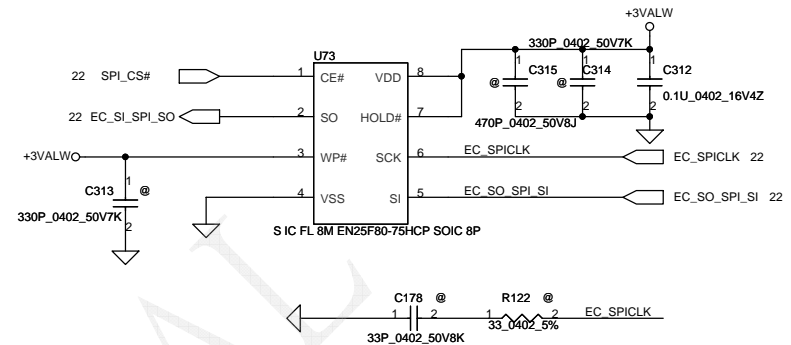
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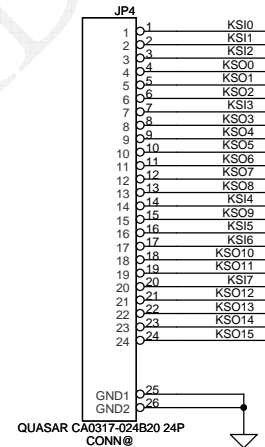
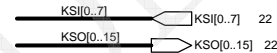
FAN Control Circuit



SPI Flash (16Mb*1)

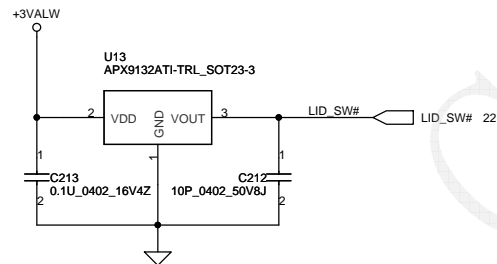


KEYBOARD CONN.

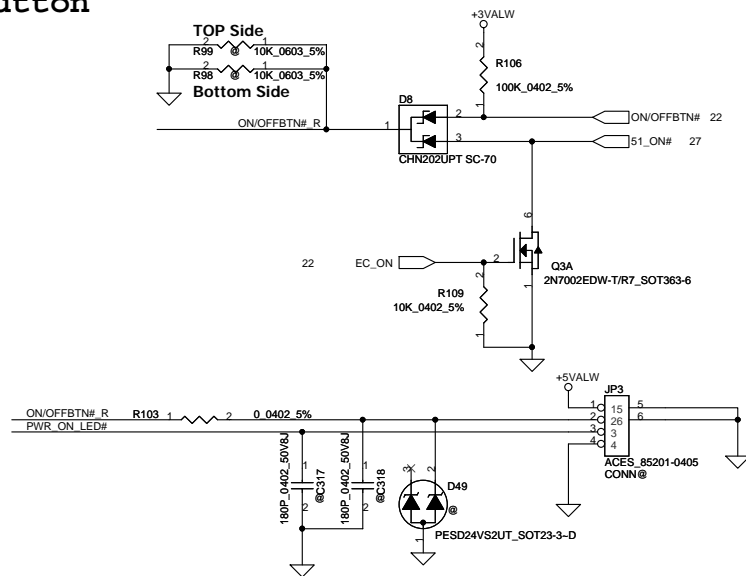


KSI0	C136	1	2	100P_0402_50V8J
KSI1	C149	1	2	100P_0402_50V8J
KSI2	C160	1	2	100P_0402_50V8J
KSI3	C159	1	2	100P_0402_50V8J
KSI4	C146	1	2	100P_0402_50V8J
KSI5	C168	1	2	100P_0402_50V8J
KSI6	C135	1	2	100P_0402_50V8J
KSI7	C155	1	2	100P_0402_50V8J
KSO0	C171	1	2	100P_0402_50V8J
KSO1	C133	1	2	100P_0402_50V8J
KSO2	C148	1	2	100P_0402_50V8J
KSO3	C170	1	2	100P_0402_50V8J
KSO4	C132	1	2	100P_0402_50V8J
KSO5	C147	1	2	100P_0402_50V8J
KSO6	C158	1	2	100P_0402_50V8J
KSO7	C169	1	2	100P_0402_50V8J
KSO8	C131	1	2	100P_0402_50V8J
KSO9	C157	1	2	100P_0402_50V8J
KSO10	C145	1	2	100P_0402_50V8J
KSO11	C166	1	2	100P_0402_50V8J
KSO12	C144	1	2	100P_0402_50V8J
KSO13	C134	1	2	100P_0402_50V8J
KSO14	C167	1	2	100P_0402_50V8J
KSO15	C156	1	2	100P_0402_50V8J

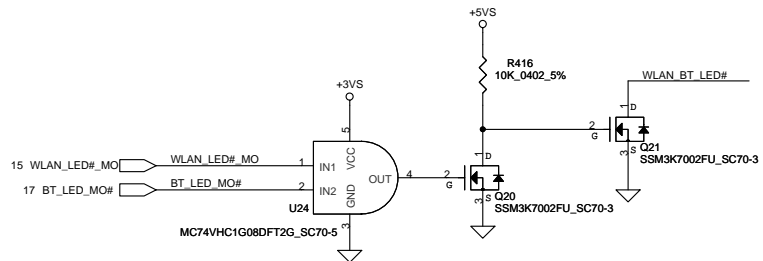
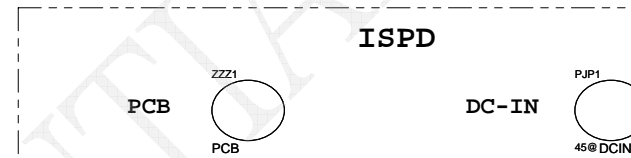
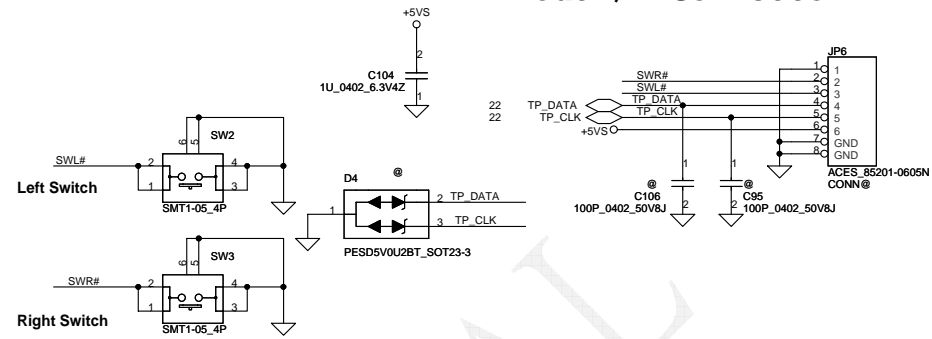
Lid SW



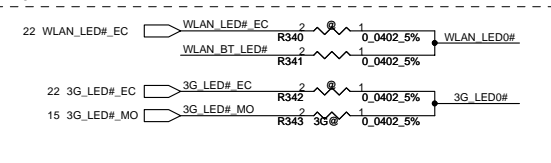
Power Button



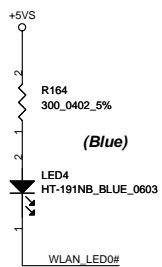
Touch/B Connector



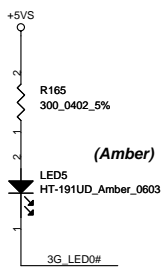
Option control LED



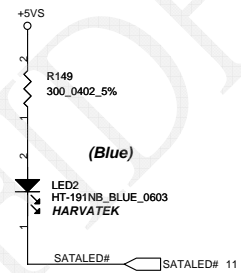
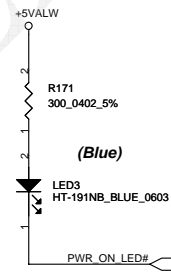
WLAN_LED



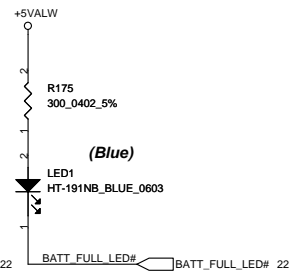
3G_LED



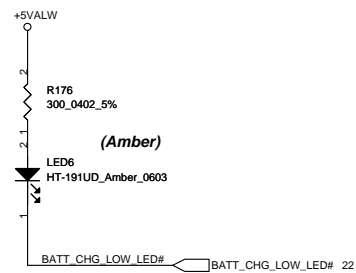
HDD_LED

**Power_LED**

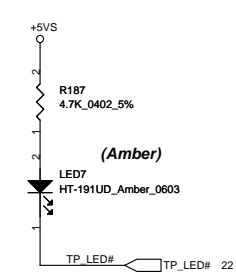
Charge_LED



Charge_LED

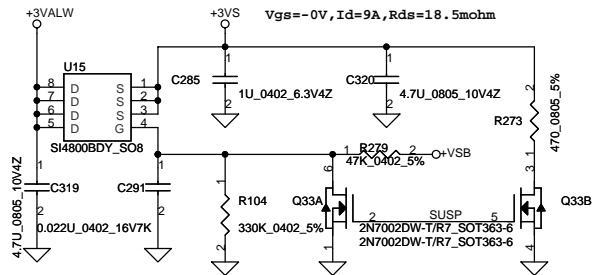


TP_LED

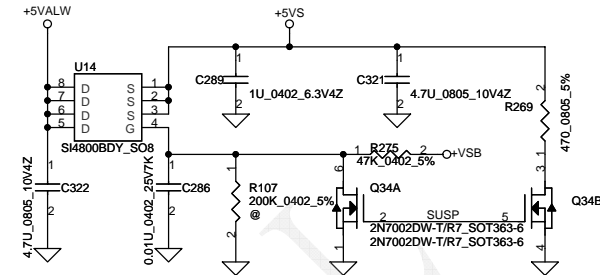


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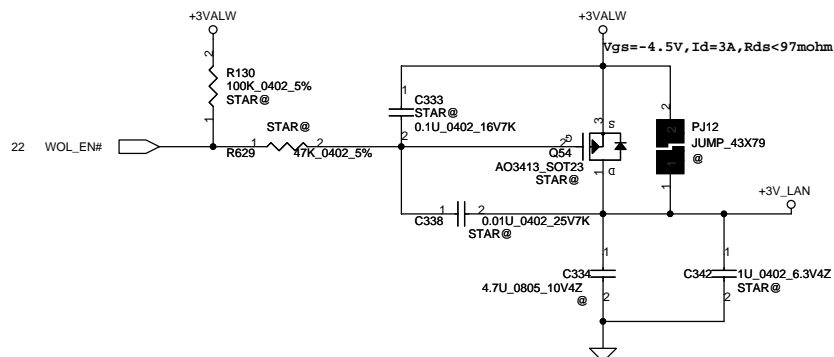
+3VALW TO +3VS



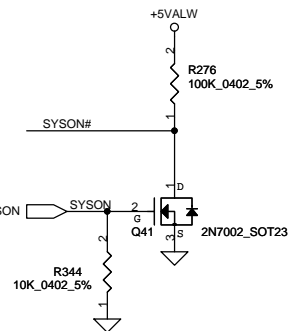
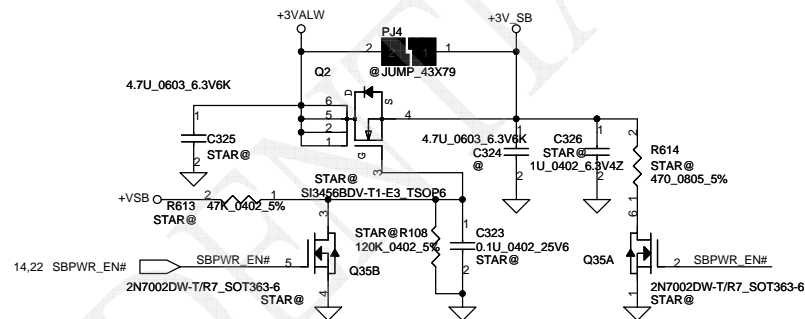
+5VALW TO +5VS



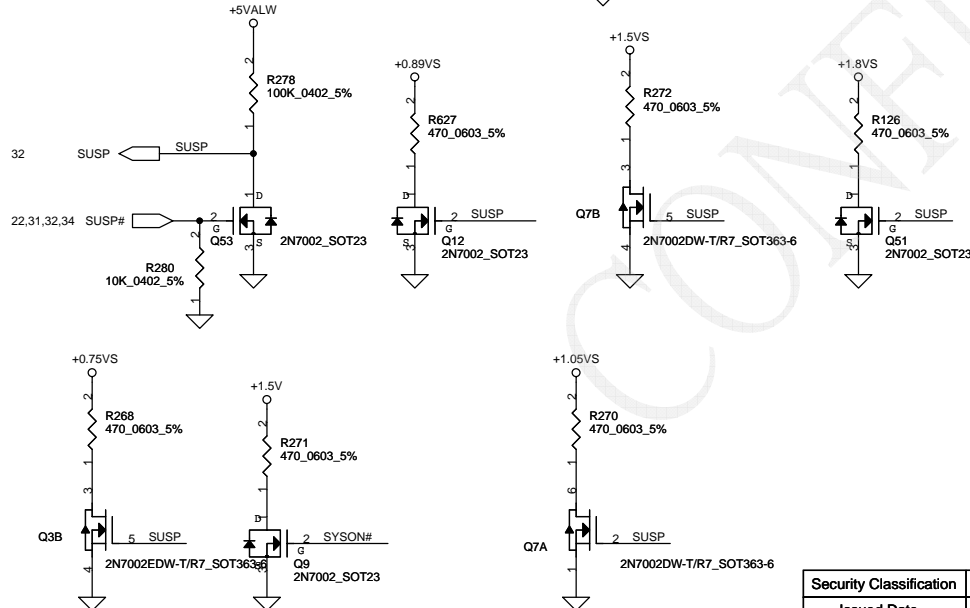
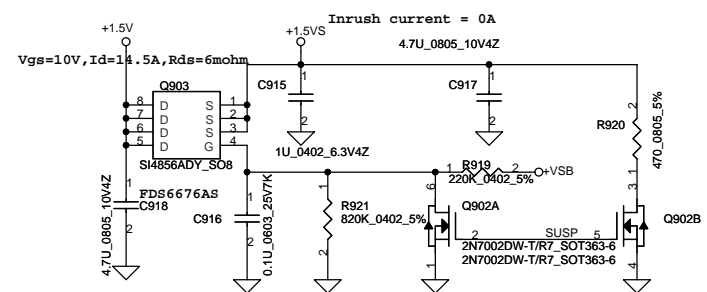
+3VALW TO +3V_LAN



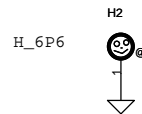
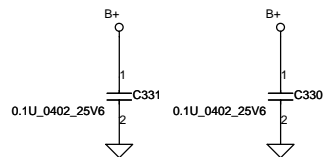
+3VALW TO +3V_SB



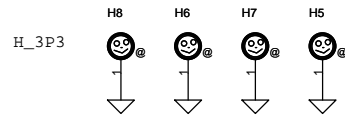
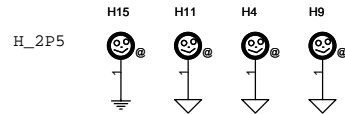
+1.5V to +1.5VS



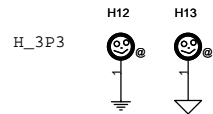
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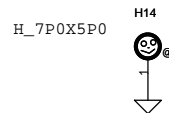
02/12 H2 change to H_6P6



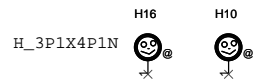
01/06 H_3P8 change to H_3P3



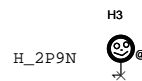
01/06 H_3P1 change to H_3P3



02/12 H_7P0X5P0N change to H_7P0X5P0



01/06 Add a H_3P1X4P1N



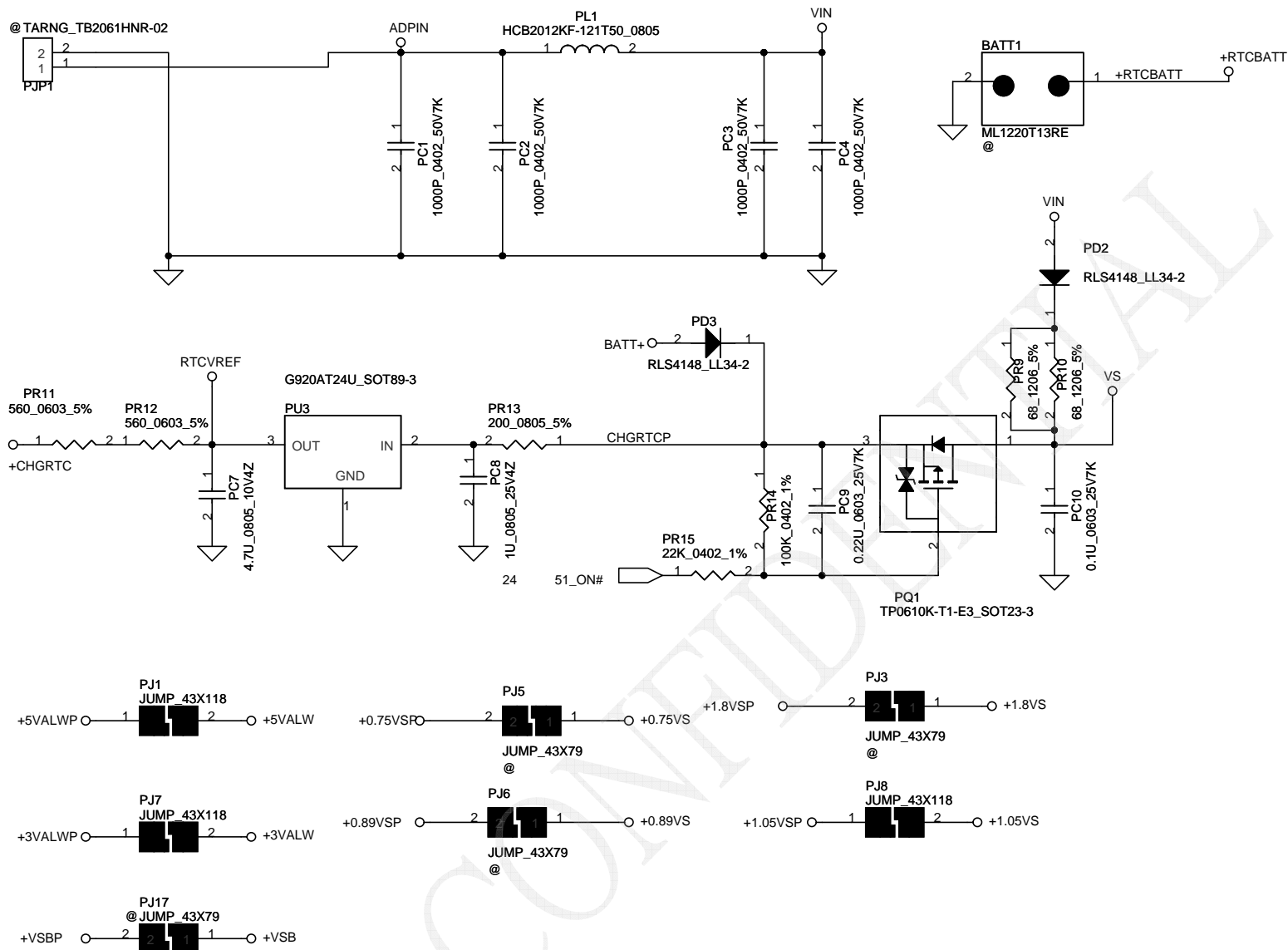
01/06 H_2P9N change to H_2P7N

03/11 H_2P7N change to H_2P9N

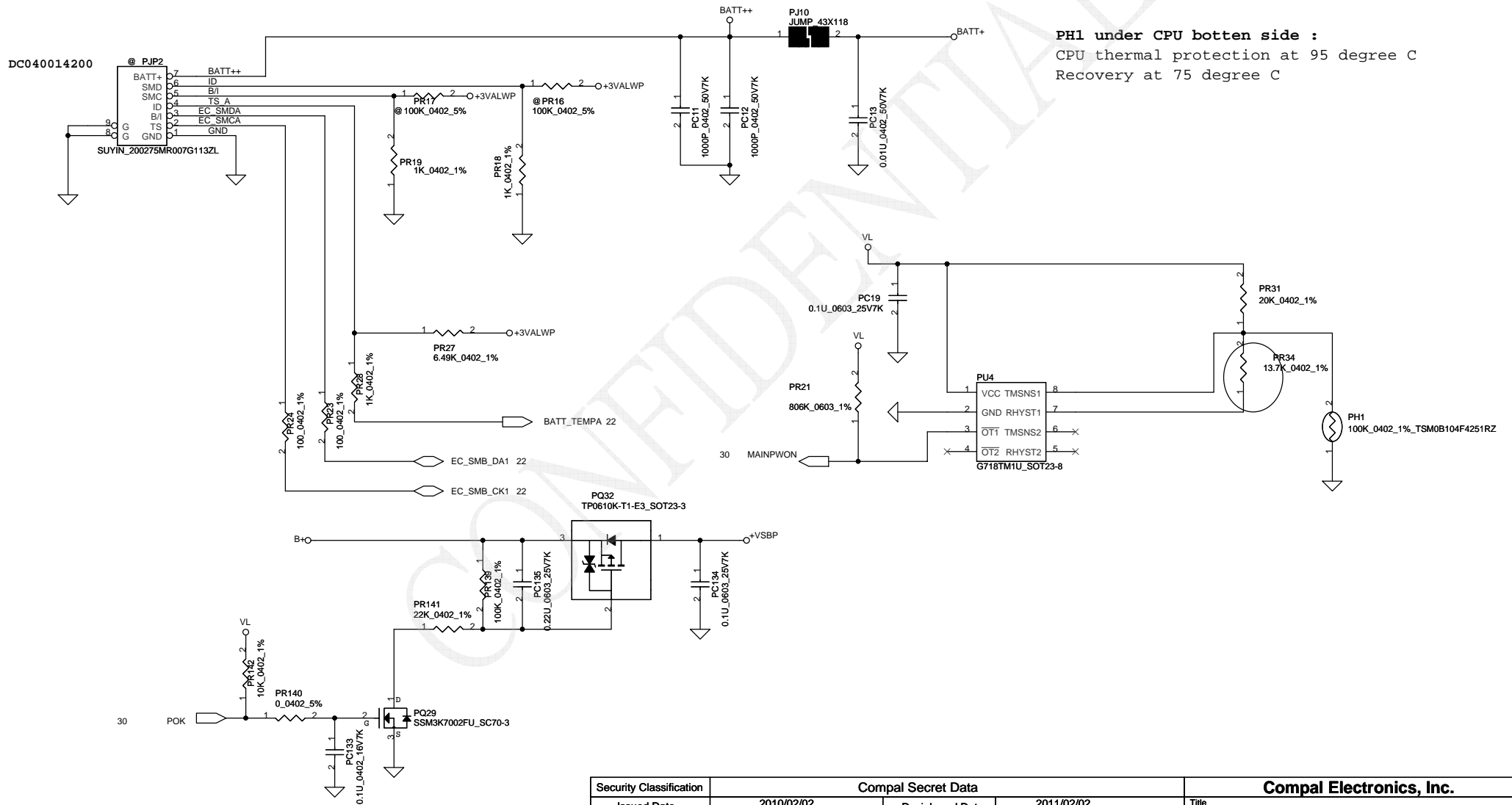


FIDUCIAL_C40M80

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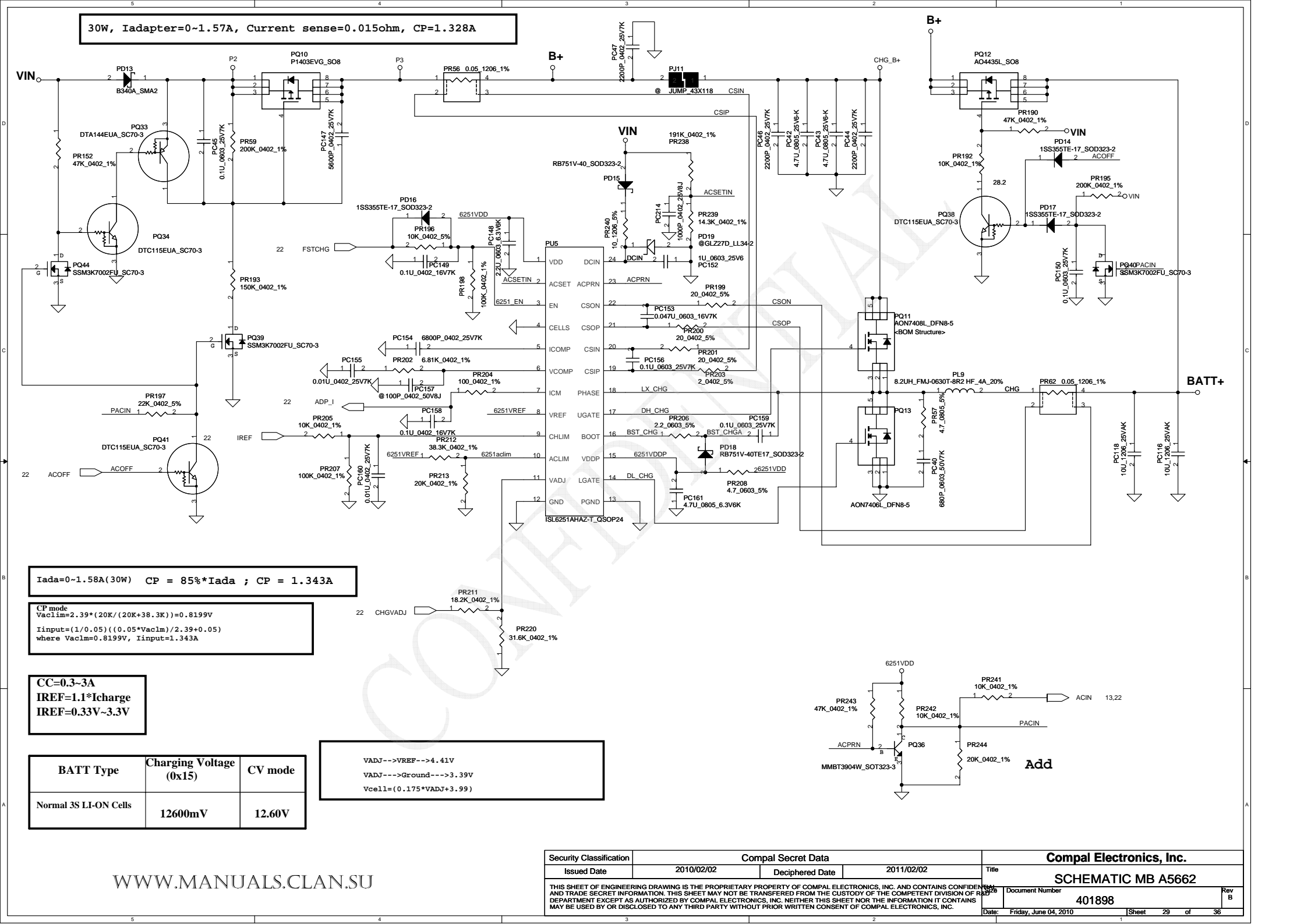


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30W, Iadapter=0~1.57A, Current sense=0.015ohm, CP=1.328A



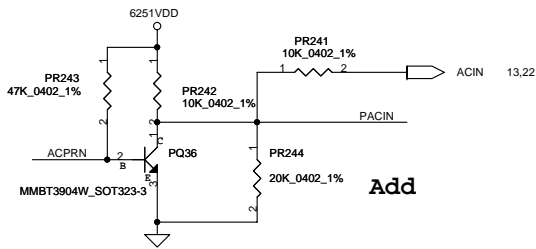
Iada=0~1.58A(30W) CP = 85%*Iada ; CP = 1.343A

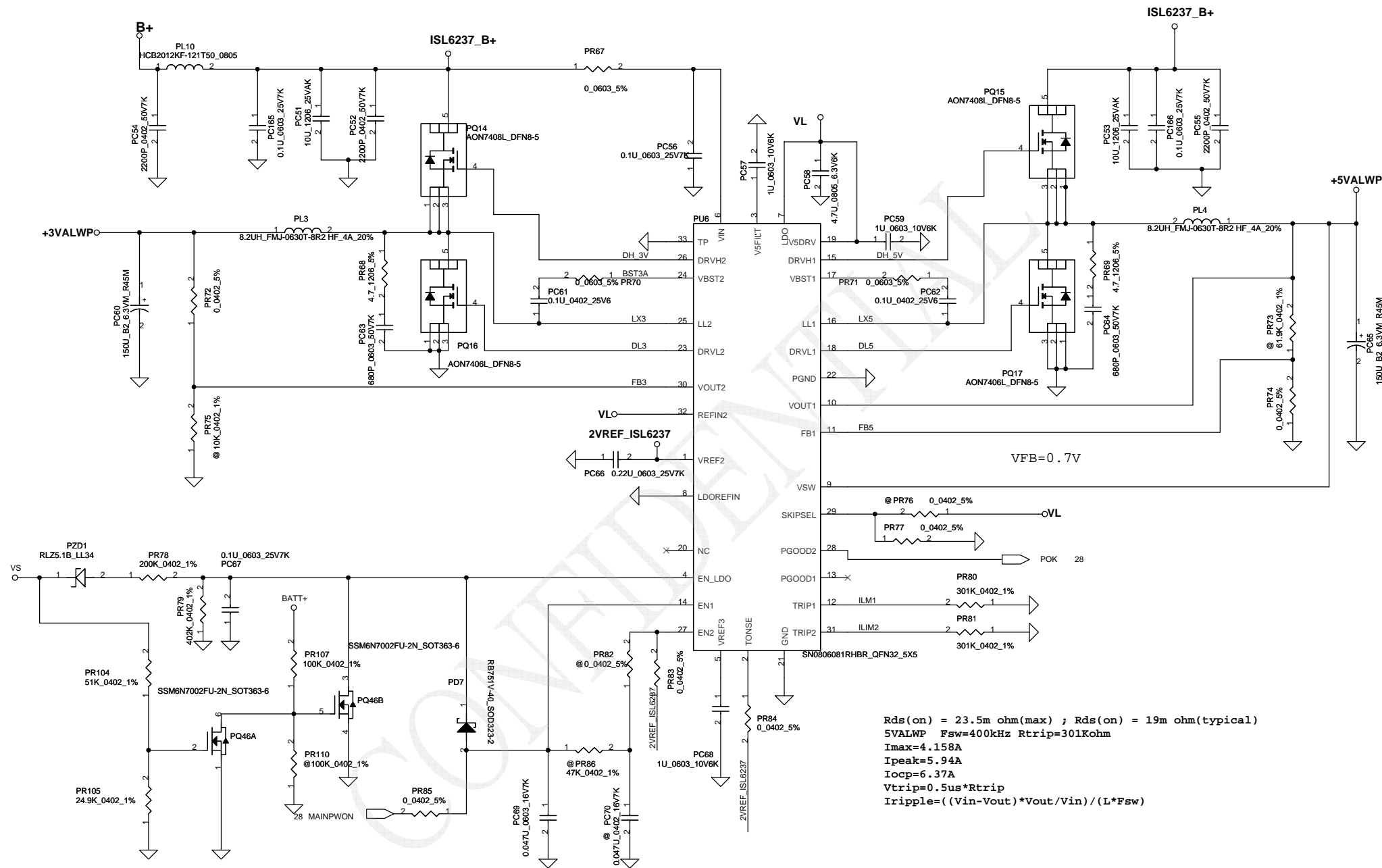
CP mode
 $V_{acim} = 2.39 * (20K / (20K + 38.3K)) = 0.8199V$
 $I_{input} = (1 / 0.05) * ((0.05 * V_{acim}) / 2.39 + 0.05)$
 where $V_{acim} = 0.8199V$, $I_{input} = 1.343A$

CC=0.3~3A
 $I_{REF} = 1.1 * I_{charge}$
 $I_{REF} = 0.33V \sim 3.3V$

BATT Type	Charging Voltage (0x15)	CV mode
Normal 3S LI-ON Cells	12600mV	12.60V

$V_{ADJ} \rightarrow V_{REF} \rightarrow 4.41V$
 $V_{ADJ} \rightarrow \text{Ground} \rightarrow 3.39V$
 $V_{cell} = (0.175 * V_{ADJ} + 3.99)$

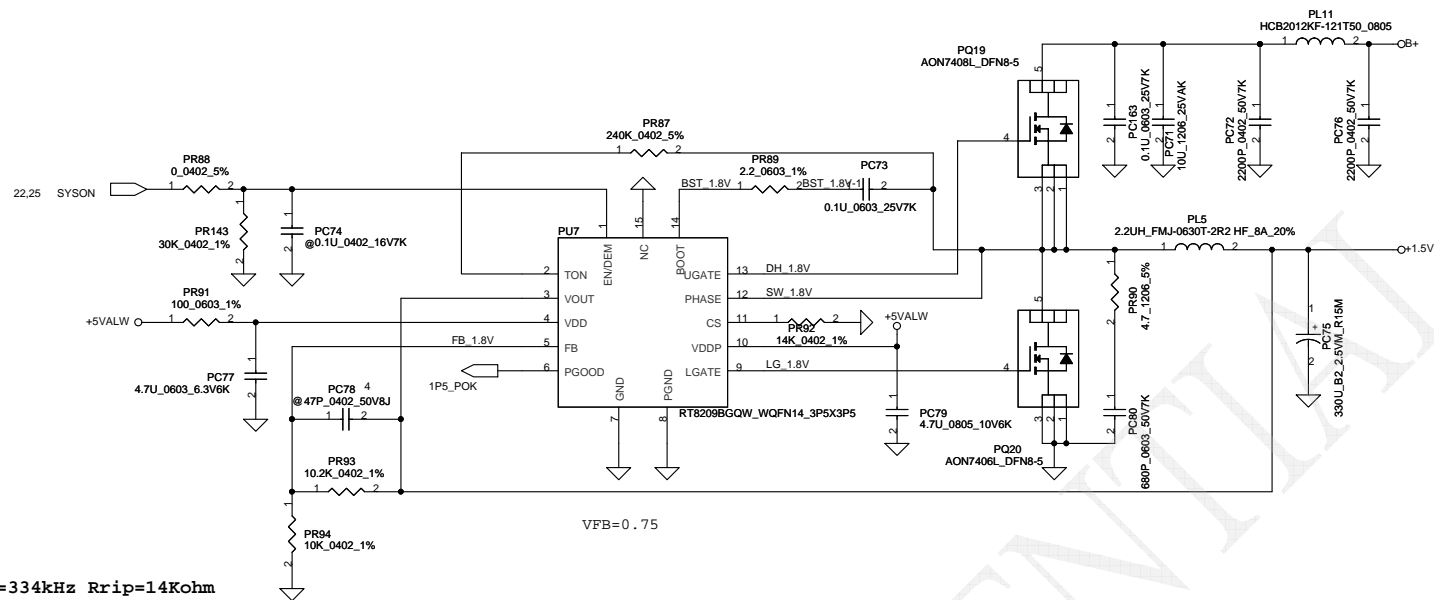




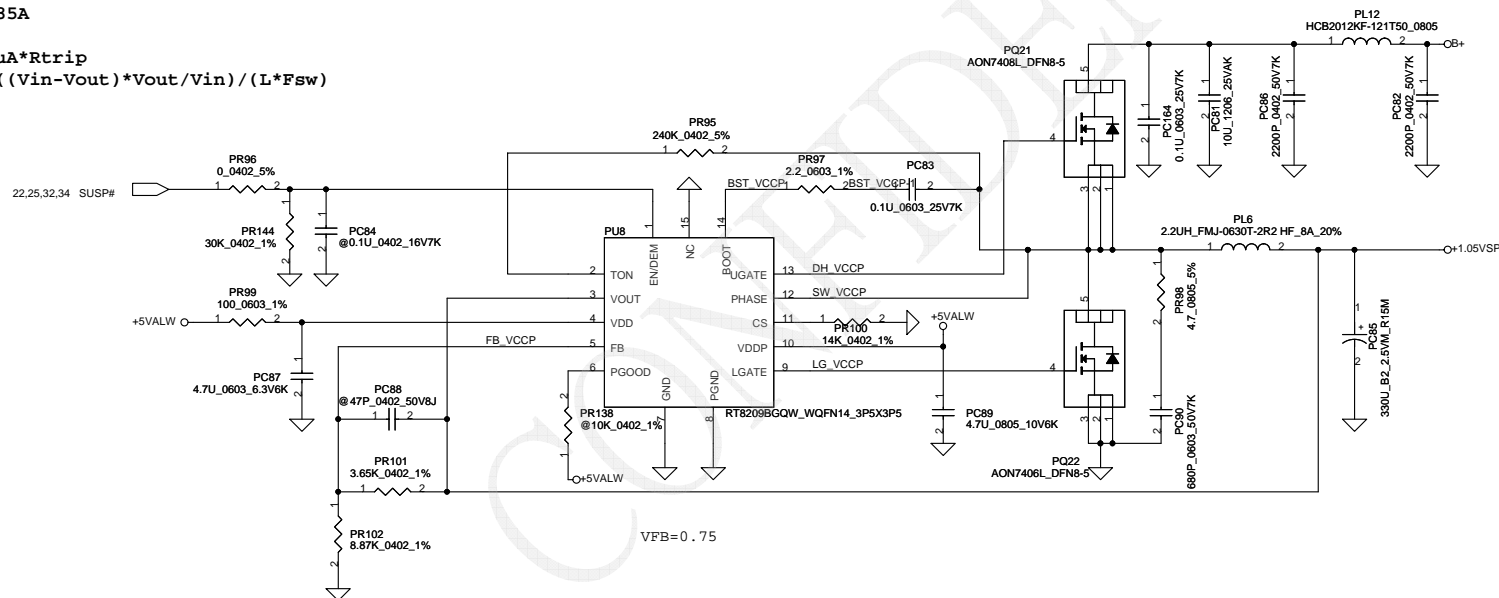
$R_{ds(on)} = 23.5m\ \Omega(max) ; R_{ds(on)} = 19m\ \Omega(typical)$
 $5VALWP\ F_{sw}=400kHz\ R_{trip}=301K\ \Omega$
 $I_{max}=4.158A$
 $I_{peak}=5.94A$
 $I_{ocp}=6.37A$
 $V_{trip}=0.5us \cdot R_{trip}$
 $I_{ripple} = ((V_{in}-V_{out}) \cdot V_{out} / V_{in}) / (L \cdot F_{sw})$

$R_{ds(on)} = 23.5m\ \Omega(max) ; R_{ds(on)} = 19m\ \Omega(typical)$
 $3.3VALWP\ F_{sw}=300kHz\ R_{trip}=301K$
 $I_{max}=4.319A$
 $I_{peak}=6.17A$
 $I_{ocp}=6.43A$
 $V_{trip}=0.5us \cdot R_{trip}$
 $I_{ripple} = ((V_{in}-V_{out}) \cdot V_{out} / V_{in}) / (L \cdot F_{sw})$

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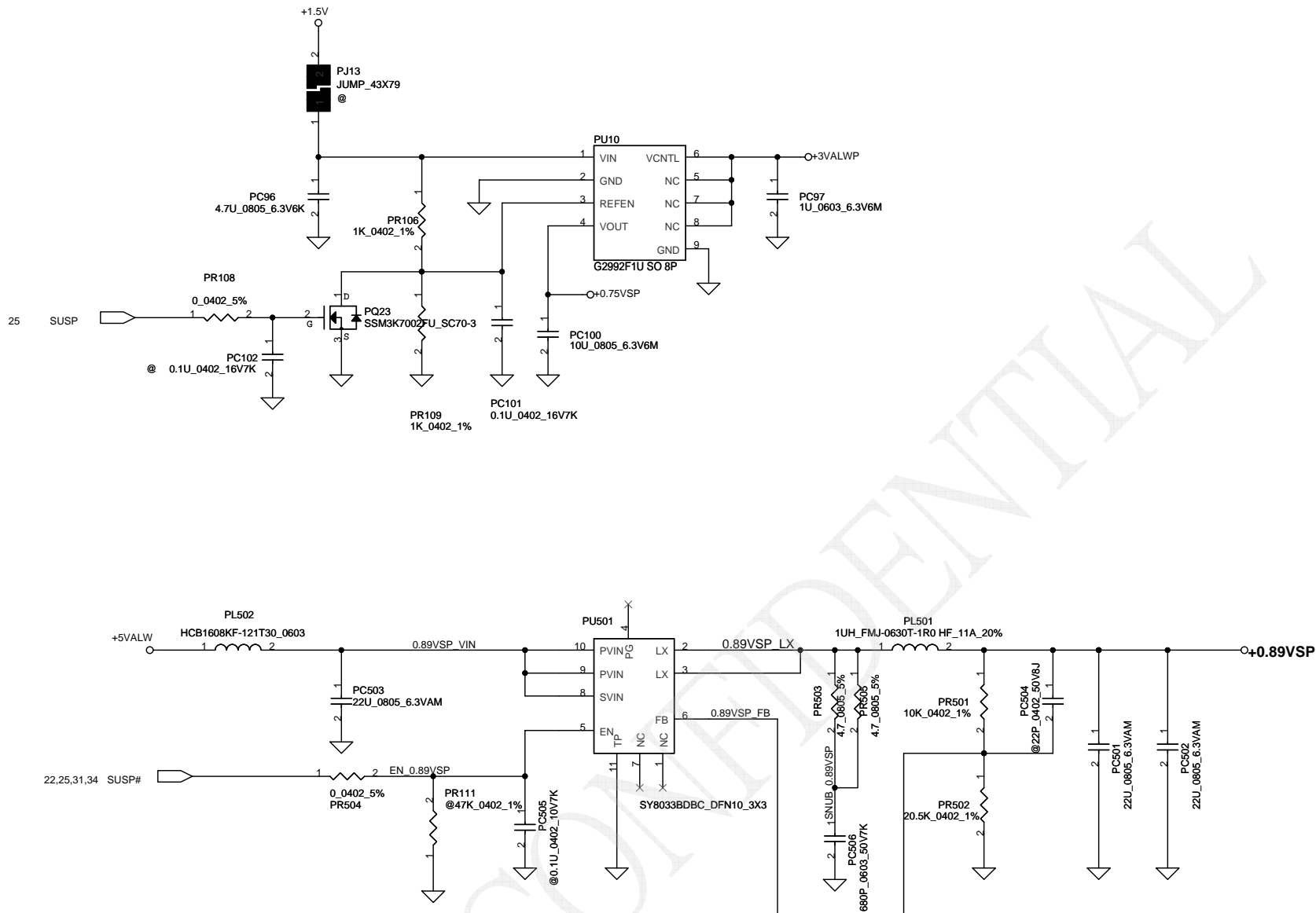
1.5VP Fsw=334kHz Rrip=14Kohm
 Imax=4.795A
 Ipeak=6.85A
 Iocp=6A
 Vtrip=10uA*Rtrip
 Iripple=((Vin-Vout)*Vout/Vin)/(L*Fsw)



VCCPP(1.05V) Fsw=338kHz Rtrip=14Kohm
 Imax=2.4493A
 Ipeak=3.499A
 Iocp=6.01A
 Vtrip=10uA*Rtrip
 Iripple=((Vin-Vout)*Vout/Vin)/(L*Fsw)

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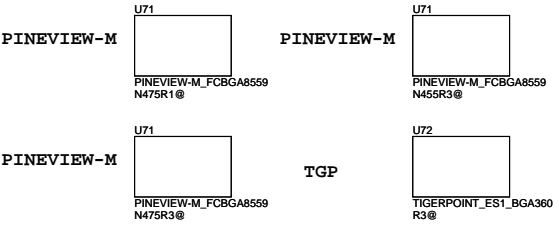
Page	Reason for change	Modify list
28	DFX issue	PQ39 from SB000006800 to SB000009610
29	DFX issue	PQ29、PQ40、PQ44 from SB000006800 to SB000009610
32	DFX issue	PQ23 from SB000006800 to SB000009610
28	let PH1 and PH4 use the same material	PH1 from SL200000V00 to SL200000U00
29	Improve switching ability	PQ13 from AON7408 to AON7406
32	SY8033 design change	PL501 from 2R2 to 1R0 PR501 from 121k to 12.1k PR502 from 249k to 24.9k
34	SY8033 design change	PR401 from 40.2k to 20k, PR402 from 20k to 10k

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PIR (Product Improve Record)

SCHEMATIC CHANGE LIST
REVISION CHANGE: 0.1--> 0.2

NO	DATE	PAGE	MODIFICATION LIST	PURPOSE
1	3/29	P.23	Mount C136,C149,C160,C159,C146,C168,C135,C155,C171,C133,C148,C170 C132,C147,C158,C169,C131,C157,C145,C166,C144,C134,C167,C156	For EMI request.
2	3/29	P.5	Remove R906,Q901,R905,C913,R907,C914 Mount R903	For EMI request.
3	5/7	P.25	change C291 from 0.01u to 0.022u	For design change.



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